

RS700-E11 Series RS700-E11-RS12U

1U Rackmount Server User Guide

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Safety information

Electrical Safety

- Before installing or removing signal cables, ensure that the power cables for the system unit and all attached devices are unplugged.
- To prevent electrical shock hazard, disconnect the power cable from the electrical outlet before relocating the system.
- When adding or removing any additional devices to or from the system, ensure that the
 power cables for the devices are unplugged before the signal cables are connected. If
 possible, disconnect all power cables from the existing system before you add a device.
- If the power supply is broken, do not try to fix it by yourself. Contact a qualified service technician or your dealer.

Operation Safety

- Any mechanical operation on this server must be conducted by certified or experienced engineers.
- Before operating the server, carefully read all the manuals included with the server package.
- Before using the server, ensure all cables are correctly connected and the power cables are not damaged. If any damage is detected, contact your dealer as soon as possible.
- To avoid short circuits, keep paper clips, screws, and staples away from connectors, slots, sockets and circuitry.
- Avoid dust, humidity, and temperature extremes. Place the server on a stable surface.
- If you encounter technical problems with the product, contact a qualified service technician or your retailer.



This product is equipped with a three-wire power cable and plug for the user's safety. Use the power cable with a properly grounded electrical outlet to avoid electrical shock.

Restricted Access Location

This product is intended for installation only in a Computer Room where:

- Access can only be gained by SERVICE PERSONS or by USERS who have been instructed about the reasons for the restrictions applied to the location and about any precautions that shall be taken.
- Access is through the use of a TOOL, or other means of security, and is controlled by the authority responsible for the location.

- Lithium-Ion Battery Warning

CAUTION! Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

CAUTION! This server system is heavy. Ask for assistance when moving or carrying the system.

Optical Drive Safety Information

Laser Safety Information





To prevent exposure to the optical drive's laser, do not attempt to disassemble or repair the optical drive by yourself. For your safety, contact a professional technician for assistance.

About this guide

Audience

This user guide is intended for system integrators, and experienced users with at least basic knowledge of configuring a server.

Contents

This guide contains the following parts:

1. Chapter 1: Product Introduction

This chapter describes the general features of the server, including sections on front panel and rear panel specifications.

2. Chapter 2: Hardware Information

This chapter lists the hardware setup procedures that you have to perform when installing or removing system components.

3. Chapter 3: Motherboard Information

This chapter gives information about the motherboard that comes with the server. This chapter includes the motherboard layout, jumper settings, and connector locations.

4. Chapter 4: BIOS Setup

This chapter tells how to change system settings through the BIOS Setup menus and describes the BIOS parameters.

5. Chapter 5: Driver Installation

This chapter provides instructions for installing the necessary drivers for different system components.

Conventions

To ensure that you perform certain tasks properly, take note of the following symbols used throughout this manual.



DANGER/WARNING: Information to prevent injury to yourself when trying to complete a task.



CAUTION: Information to prevent damage to the components when trying to complete a task.



IMPORTANT: Instructions that you MUST follow to complete a task.



NOTE: Tips and additional information to help you complete a task.

Typography

| Bold text | Indicates a menu or an item to select. |
|---|---|
| Italics | Used to emphasize a word or a phrase. |
| <key></key> | Keys enclosed in the less-than and greater-than sign means that you must press the enclosed key. |
| | Example: <enter> means that you must press the Enter or Return key.</enter> |
| <key1>+<key2>+<key3></key3></key2></key1> | If you must press two or more keys simultaneously, the key names are linked with a plus sign (+). |
| | Example: <ctrl>+<alt>+</alt></ctrl> |
| Command | Means that you must type the command exactly as shown, then supply the required item or value enclosed in brackets. |
| | Example: At the DOS prompt, type the command line: format A:/S |
| | |

References

Refer to the following sources for additional information and for product and software updates.

1. ASUS Control Center (ACC) user guide

This manual tells how to set up and use the proprietary ASUS server management utility. Visit <u>asuscontrolcenter.asus.com</u> for more information.

2. ASUS websites

The ASUS websites worldwide provide updated information for all ASUS hardware and software products. Refer to the ASUS contact information.

Product Introduction

1

This chapter describes the general features of the server. It includes sections on front panel and rear panel specifications.

1.1 System package contents

Check your system package for the following items.

| | RS700-E11-RS12U | | |
|----------------|--|--|--|
| Chassis | ASUS 1U Rackmount Chassis | | |
| Motherboard | ASUS Z13PP-D32 Server Board | | |
| Components | 1 x 80PLUS Power Supply 1 x 2.5-inch Storage Device Backplane 12 x 2.5-inch Storage Device Trays or Dummy Trays 1 x Front Panel Board 2 x Riser Cards 9 x System Fans | | |
| Accessories | 1 x Support DVD 1 x Bag of Screws 2 x CPU Heatsinks 2 x AC Power Cables 1 x External Fan Module (Optional) 1 x OCP3.0 Module (Optional) 1 x LAN Module (Optional) | | |
| Optional Items | 1 x 80PLUS Power Supply (Second PSU) 1 x Friction Rail Kit 1 x Ball Bearing Rail Kit (1000mm) 1 x Ball Bearing Rail Kit (1200mm) 1 x Cable Management Arm (1200m Cabinet only) | | |

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• If any of the above items is damaged or missing, contact your retailer.

Optional items come bundled if you selected them when purchasing the system and cannot be bought separately.

1.2 Serial number label

The product's serial number contains 12 characters, such as xxSxxxxxxx, and printed on the sticker adhered to the server's front cover.

The correct serial number of the product is required if you need to request for support from the ASUS Technical Support team.



1.3 System specifications

The ASUS RS700-E11 Series features the ASUS Z13PP-D32 server board. The server supports 4th Generation Intel® Xeon® Scalable Processors, plus other latest technologies through the chipsets onboard.

| Model Name | | RS700-E11-RS12U |
|-------------------|--------------------------------|--|
| Motherboard | | Z13PP-D32 |
| Processor Support | | 2 x Socket E (LGA 4677) |
| | | 4th Generation Intel® Xeon® Scalable Processors |
| Core Logic | | Intel® C741 Chipset |
| | Total Slots | 32 (8-channel per CPU, 16 DIMM per CPU) |
| | Capacity | Maximum up to 4TB (DDR5) + 8TB (Crow Pass) |
| | | DDR5 4800/4400 RDIMM / RDIMM 3DS |
| | Memory Type | Intel [®] Optane [™] DC persistent memory 300 series (Crow Pass) |
| Memory | | * Please refer to <u>www.asus.com</u> for latest momory AVL update |
| | | 64GB, 32GB, 16GB RDIMM |
| | | 256GB, 128GB RDIMM 3DS |
| | Memory Size | 512GB, 256GB, 128GB Intel [®] Optane [™] DC persistent memory 300 Series (Crow Pass) |
| | | * Refer to www.asus.com/support for more information |
| | Total PCIe / HBA/RAID Slots | 3+1 |
| | Slot Type | Up to 3 PCIe Gen5 slots |
| | | 1 x PCle x16 (Gen5 x16 link), FHFL (CPU1) |
| | | 1 x PCle x16 (Gen5 x16 link), FHFL (CPU1) or OCP3.0 (CPU1) |
| Expansion | | 1 x PCle Gen5 x16, LP (CPU2) |
| Slots | M.2 | 2 x M.2 (only supports PCIe mode 2260 or 2280 from CPU1) |
| | Micro SD Card slot | 1 (supported by Aspeed AST2600) |
| | Proprietary Slot 1 | 1 x PCle x16 (Gen5 x8 link), LP (Internal Slot for HBA/RAID card) (CPU2) |
| | Proprietary Slot 2 | 4 x 1GbE RJ45 LAN Module Card (I350-AM4) or |
| | | 2 x 10GbE RJ45 LAN Module Card (X710-AT2) |
| | SATA Controller | 12 x SATA 6Gb/s ports |
| Disk | | Optional kits: |
| Controller | SAS Controller | Broadcom HBA card 9500-8i |
| | SAS Controller | Broadcom MegaRAID card 9540-8i |
| | | Broadcom MegaRAID card 9560-16i |
| | | 12 x 2.5" Hot-Swap Drive Bays: |
| Storage | Storage Bay | - 12 x NVMe/SAS*/SATA |
| | | * SAS support only from optional SAS HBA/RAID card * Support tri-mode card (only for storage bays 9 - 12) |

(continued on the next page)

| Model Name | | RS700-E11-RS12U |
|------------------|--------------------|--|
| | Motherboard | 2 x M.2 connectors |
| | onboard connectors | 2 x SlimSAS x8 connectors |
| | Default Cables | 2 x SlimSAS (1 x8) to SlimSAS (2 x4) Y cables |
| Storage | | Supports 4 x NVMe: via 2 x MCIO cables |
| | NVMe upgrade | Supports 8 x NVMe: via 4 x MCIO cables |
| | option | Supports 12 x NVMe: via 6 x MCIO cables |
| | | * Please refer to Asus server Upgrade Part List for the latest update |
| | | 4 x 1Gbe (Intel® I350-AM4) RJ45 ports or |
| Networking | | 1 x Management Port |
| Networking | | Ontional OCP 3.0 Adapter: |
| | | Un to 200Gb/s Ethernet / InfiniBand Adapter |
| VGA | | Aspeed AST2600 64MB |
| | | Up to 2 single slot CPUs or 1 duel slot CPU* |
| Graphic | | * The external fan must be installed for dual slot GPU |
| Front I/O Por | ts. | |
| | | 2 x LISE 2.2 Cont. porto |
| | | 1 x VGA port |
| | | 1 x B L45 Mamt LAN port |
| Rear I/O Port | s | 1/2 x NIC norte* |
| | | 1 x OCP 3.0 port (ontional) |
| | | * The number of NIC ports available depends on the LAN Controller |
| | | card installed. |
| | | Front Switch/LED: |
| | | 1 x Power Switch (w/ LED) |
| | | 1 x Reset Switch |
| | | 1 x Location Switch (w/ LED) |
| Switch/LED | | 1 x Message LED |
| | | LAN 1/3 and 2/4 LED (on NIC module)* |
| | | The number of LAN LEDs available depends on the LAN Controller card installed. |
| | | Rear Switch/LED: |
| | | 1 x Port 80 LED (Q-Code) |
| | | 1 x Power Switch w/ LED |
| | | 1 x Location Switch w/ LED |
| Security Opt | ions | TPM-SPI |
| Security Options | | PFR |

(continued on the next page)

| Model Name | | RS700-E11-RS12U |
|---|-------------------------------------|---|
| OS Support | | Windows® Server RedHat® Enterprise Linux SuSE® Linux Enterprise Server CentOS Ubuntu * Please find the latest OS support from <u>https://www.asus.com/</u> |
| | Software | ASUS Control Center |
| Management Solution | Out of Band Remote Management | On-Board ASMB11-iKVM for KVM-over-IP |
| Regulatory Co | mpliance | BSMI, CE, CB, FCC (Class A) |
| Dimension | | 842.5mm x 439.5mm x 44mm (1U) 33.17" x 17.3" x 1.73" |
| Net Weight Kg | | 13.7 kg (CPU, DRAM & HDD not included) |
| Gross Weight Kg | | 22.56 kg (CPU, DRAM & HDD not included, Packing included) |
| Power Supply (different configuration by region) | | 1+1 Redundant 1200W 80 PLUS Platinum Power Supply Rating: 220-240Vac, 10A (x2), 50/60Hz or 1+1 Redundant 1600W 80 PLUS Platinum Power Supply Rating: 100-127/220-240Vac, 10A/8A (x2), 50/60Hz or 1+1 Redundant 1600W 80 PLUS Titanium Power Supply Rating: 100-127Vac/220-240Vac, 12A/9.5A (x2), 50/60Hz |
| Environment | | Operating temperature: 10° ~ 35° or 10° ~ 30° if dual slot GPU is used Non-operating temperature: -40° ~ 60° Non-operating humidity: 20% ~ 90% (Non-condensing) |

*Specifications are subject to change without notice.

1.4 Front panel features

The barebone server displays a simple yet stylish front panel with easily accessible features. The power and reset buttons and LED indicators are located on the front panel.



• All bays support 2.5" drives with trays.

For extra security, a front bezel (purchased separately) can be installed to prevent unauthorized physical access to the hard drives and power button.



1.5 Rear panel features

The rear panel includes expansion slots and system power sockets. The middle part includes the I/O shield with openings for the rear panel connectors on the motherboard.



1.6 Internal features

The barebone server includes the basic components as shown.



- 1. Redundant power supply
- 2. ASUS Z13PP-D32 server board
- 3. System fans
- 4. 12 x 2.5" storage device trays
- 5. NVMe/SATA/SAS backplane (hidden)
- 6. PCle riser card (hidden)
- 7. Butterfly riser card
- 8. Asset tag (hidden)
- 9. External fan (optional, for GPU)
- 10. OCP module (hidden, optional)



The barebone server does not include a floppy disk drive. Connect a USB floppy disk drive to any of the USB ports on the front or rear panel if you need to use a floppy disk.

A protection film is pre-attached to the front cover before shipping. Please remove the protection film before turning on the system for proper heat dissipation.

HAZARDOUS MOVING PARTS KEEP FINGERS AND OTHER BODY PARTS AWAY

1.7 LED information

1.7.1 Front panel LEDs



| LED | Display status | Description |
|--------------|-----------------------|--|
| Power LED | ON | System power ON |
| Message LED | OFF ON | System is normal; no incoming event A hardware monitor event is indicated |
| Location LED | OFF ON | Normal status Location switch is pressed (Press the location switch again to turn off) |
| LAN LEDs | OFF Blinking ON | No LAN connection LAN is transmitting or receiving data LAN connection is present |

1.7.2 Storage device status LED



| SATA/SAS Storage Device LED Description | | | | |
|---|----------|---|--|--|
| GREEN | ON | SATA/SAS storage device power ON | | |
| RED | ON | Storage device has failed and should be swapped immediately (For RAID card) | | |
| GREEN/ RED | Blinking | RAID rebuilding (For RAID card) | | |
| GREEN/ RED | Blinking | Locate (For RAID card) | | |
| GREEN/ RED | OFF | Storage device not found | | |
| GREEN | Blinking | Read/write data from/into the SATA/SAS/NVMe storage device | | |

1.7.3 LAN (RJ45) LEDs

Intel® I350-AM4 1GbE LAN port LEDs

ACT/LINK LED SPEED LED



| ACT / LINK LED | | SPEED LED | | |
|----------------|---------------|-----------|---------------------|--|
| Status | Description | Status | Description | |
| OFF | No link | OFF | 10Mbps connection | |
| GREEN | Linked | ORANGE | 100 Mbps connection | |
| BLINKING | Data activity | GREEN | 1 Gbps connection | |

Intel® X710-AT2 10GbE LAN port LEDs



| ACT / LINK LED | | SPEED LED | | |
|----------------|---------------|-----------|------------------------------|--|
| Status | Description | Status | Description | |
| OFF | No link | OFF | 10Mbps / 100 Mbps connection | |
| GREEN | Linked | ORANGE | 1 Gbps connection | |
| BLINKING | Data activity | GREEN | 10 Gbps connection | |

1.7.4 Rear panel LEDs



Q-Code LED

| LED | Display status | Description |
|--------------|----------------|---|
| Power LED | ON | System power ON |
| | OFF | Normal status |
| Location LED | ON | Location switch is pressed (Press the location switch again to turn off) |

1.7.5 Q-Code table

| ACTION | PHASE | POST CODE | TYPE | DESCRIPTION |
|--------------|-----------------------------|-----------|----------|--|
| SEC Start up | | 0x01 | Progress | Power on post code |
| | | 0x02 | Progress | Load BSP microcode |
| | Security Phase | 0x03 | Progress | Perform early platform cache Initialization |
| | | 0x04 | Progress | Set cache as ram for PEI phase |
| | | 0x05 | Progress | Establish Stack |
| | | 0x06 | Progress | CPU Early Initialization |
| | | 0x10 | Trogress | PELCore Entry |
| | | 0x10 | Progress | PEI cache as ram CPI Linitial |
| | initialization)phase | 0x15 | | NB Initialization before installed memory |
| | | 0x10 | | SB Initialization before installed memory |
| | | 0xC8 | | |
| | | 0xCC | - | Infineon Address |
| | | 0xD4 | | |
| | | | | |
| | VR initialization | 0xB8 | Progress | |
| | | 0xBC | | Ti Address |
| | | 0xB0 | | |
| | | 0xB0 | | |
| | | 0x04 | | Enter OCMP Breeduree |
| | | 0x11 | | Enter OCMR Procedules |
| | | 0x12 | | Charle New ODU |
| | | 0x13 | | Check New CPU |
| | | 0x14 | | |
| | | 0x10 | | |
| | | 0x18 | | Prepare Parameters |
| | | 0x21 | Progress | Build Voltage Table |
| | OCMR initialization | 0x22 | | Patch Voltage Table |
| | | 0x23 | | Adjust Voltage Table |
| | | 0x24 | - | Before Set Voltages |
| | | 0x25 | | Set Voltages |
| | | 0x31 | | Before Set Spread Spectrum |
| | | 0x32 | | SetBclkStrapAndFrequencyPei |
| | | 0x33 | | Set Spread Spectrum |
| Quick VGA | | 0x34 | | After Set Frequency |
| | | 0xA0 | _ | Initialize KTI input structure |
| | | 0xA1 | | Collect info such as SBSP, Boot Mode, Reset type |
| | | 0xA3 | | Setup up minimum path between SBSP & other sockets |
| | | 0xA6 | | Sync up with PBSPs |
| | | 0xA7 | | Topology discovery and route calculation |
| | KTI initialization | 0xA8 | Progress | Program final route |
| | | 0xA9 | | Program final IO SAD setting |
| | | 0xAA | | Protocol layer and other Uncore settings |
| | | 0xAB | | Transition links to full speed opeartion |
| | | 0xAE | | Coherency Settings |
| | | 0xAF | | KTI Complete |
| | | 0xE0 | | IIO early init |
| | | 0xE1 | | Early Pre-link training setting |
| | | 0xE2 | | IIO Gen3 EQ programming |
| | | 0xE3 |] | IIO Link training |
| | | 0xE4 | | IIO Gen3 override |
| | | 0xE5 | Progress | IIO early init exit |
| | | 0xE6 | | IIO late init |
| | IIO Early initialization | 0xE7 | | PCIE port init |
| | | 0xE8 | | IOAPIC init |
| | | 0xE9 | | VTD init |
| | | 0xEA | | IOAT init |
| | | 0xEB | | IIO DFX init |
| | | 0xEC | | NTB init |
| | | 0xED | | Security init |
| | | 0xEE | | IIO late init exit |
| | | 0xEF | | IIO On ready to boot |

(continued on the next page)

| ACTION | PHASE | POST CODE | TYPE | DESCRIPTION |
|-------------|---|-----------|----------|--|
| | | 0x70 | | High Bandwidth Memory |
| | | 0x7E | | Pine Sync AP Boot Mode |
| | | 0xB0 | | Detect DIMM Configuration |
| | | 0xB1 | - | Initialize clocks for all MemSs |
| | | 0xB1 | | Gather SPD Data |
| | | 0xB2 | | Early Configuration |
| | | 0xB0 | | Check DIMM Panks |
| | | 0xD4 | - | Barallel Made Diapateh |
| | | 0xB5 | | Parallel Mode Dispatch |
| | | 0.07 | | |
| | | 0,00 | | |
| | | 0,00 | | |
| | | 0xB9 | - | Memory Test |
| | | UXBA | | Memory Init |
| | | UXBB | | |
| | | 0xBC | | Set HAS Configuration |
| | MRC Memory | 0xBD | Progress | Get Margin |
| | initialization | 0xBE | | BIOS SSA Initialization |
| | | 0xBF | | MRC Done |
| | | 0xC1 | | Check POR Compatibility |
| | | 0xC2 | | Unlock Memory |
| | | 0xC3 | | Check Status |
| | | 0xC4 | | Check XMP |
| | | 0xC5 | | Initialize Memory |
| | | 0xC6 | | Socket DIMM Information |
| | | 0xC7 | | Prep NVDIMM for Training |
| | | 0xC9 |] | Setup SVL and Scrambling |
| QUICK VGA | | 0xCA |] | Init CMI Credit Programming |
| | | 0xCB |] | Check Ras Support After MemInit |
| | | 0xCC |] | Initialize ADR |
| | | 0xCD | 1 | Init Structures Late |
| | | 0xCE | - | Memory Late |
| | | 0xCF | | Select Boot Mode |
| | | 0x32 | - | CPU POST-Memory Initialization |
| | | 0x33 | | CPU Cache Initialization |
| | | 0x34 | | Application Processor(s) (AP) Initialization |
| | | 0x35 | | BSP Selection |
| | DXE (Driver Execution Environment)phase | 0x36 | | |
| | | 0x37 | | Pre-memory NB Initialization |
| | | 0x3B | | Pro-momony SB Initialization |
| | | 0x4E | - | DVE Initial Program Load/IPL) |
| | | 0x40 | | DXE (niuai riografii Eoad(ii E) |
| | | 0x00 | | DVE NV/DAM Initialization |
| | | 0x61 | Progress | CR run time Initialization |
| | | 0x62 | | OPU DVC laikalization |
| | | 0x63 | | CPU DXE Initialization |
| | | 0x68 | | PGI HB Initialization |
| | | 0x69 | - | NB DXE Initialization |
| | | UXBA | | IND DAE SMM INITIALIZATION |
| | | 0x70 | | SB DXE Initialization |
| | | 0x/1 | | SB DXE SMM Initialization |
| | | 0x72 | | SB DEVICES Initialization |
| | | Ux/8 | | ACPI Module Initialization |
| | | 0x79 | | CSM Initialization |
| | | 0x90 | | BDS started |
| | | 0x91 | Progress | Connect device event |
| | BDS (Boot Device Selection) phase | 0x92 | | PCI Bus Enumeration |
| | | 0x93 | | PCI Bus Enumeration |
| | | 0x94 | | PCI Bus Enumeration |
| Normal boot | | 0x95 | | PCI Bus Enumeration |
| | | 0x96 | | PCI Bus Enumeration |
| | | 0x97 | | Console output connect event |
| | | 0x98 | | Console input connect event |
| | | 0x99 | | AMI Super IO start |
| | | 0x9A | | AMI USB Driver Initialization |

(continued on the next page)

| ACTION | PHASE | POST CODE | TYPE | DESCRIPTION |
|-------------|--------------------------------------|-----------|----------|----------------------------------|
| | | 0x9B | | AMI USB Driver Initialization |
| | | 0x9C | | AMI USB Driver Initialization |
| | | 0x9D |] | AMI USB Driver Initialization |
| | | 0xA0 | 1 | AHCI Initialization |
| | | 0xA1 |] | AHCI Initialization |
| | | 0xA2 |] | AHCI Initialization |
| | | 0xA3 |] | AHCI Initialization |
| | BDS (Boot Device Selection) phase | 0xA8 |] | BIOS Setup password verify |
| | | 0xA9 | Progress | BIOS Setup start |
| Normal boot | | 0xAB | | BIOS Setup input wait |
| | | 0xAD | | Ready to Boot event |
| | | 0xAE | | Legacy Boot event |
| | | 0xAF | | Exit Boot Services |
| | | 0xB2 | | Legacy Option ROM Initialization |
| | | 0xB3 | | Reset system |
| | | 0xB4 | | USB Hotplug |
| | | 0xB5 | | PCI Bus Hotplug |
| | | 0xB6 | | NVRAM clean up |
| | | 0xB7 | | NVRAM configuration reset |



Hardware Information

This chapter lists the hardware setup procedures that you have to perform when installing or removing system components.

2.1 Chassis cover

2.1.1 Removing the rear cover

- 1. Remove the two (2) screws (one on each side of the cover) with a Phillips screwdriver.
- 2. Loosen the thumbscrew on the rear panel to release the cover from the chassis.
- 3. Firmly hold the cover and slide it towards the rear panel about half an inch until it is disengaged from the chassis.
- 4. Lift the cover from the chassis.



2.1.2 Removing the backplane cover

- 1. Remove the two (2) screws (one on each side of the cover) with a Phillips screwdriver.
- 2. Hold both ends of the cover (A) and lift from the chassis (B).



2.2 Air duct(s)

2.2.1 Removing the air duct

Gently lift the air duct vertically out of the chassis.



2.2.2 Installing the air duct(s)

Align the air duct along the edges of the DIMM slots, and then place the air duct in the chassis



2.3 Central Processing Unit (CPU)

The motherboard comes with a surface mount Socket E designed for the 4th Generation Intel[®] Xeon[®] Scalable Processors.

| • | Upon purchase of the motherboard, ensure that the PnP cap is on the socket and the socket contacts are not bent. Contact your retailer immediately if the PnP cap is missing, or if you see any damage to the PnP cap/socket contacts/motherboard components. ASUS will shoulder the cost of repair only if the damage is shipment/ transit-related. |
|---|--|
| • | Keep the cap after installing the motherboard. ASUS will process Return Merchandise |

- Authorization (RMA) requests only if the motherboard comes with the cap on the Socket E+.
- The product warranty does not cover damage to the socket contacts resulting from incorrect CPU installation/removal or misplacement/loss/incorrect removal of the PnP cap.

2.3.1 Removing the CPU and heatsink (for Type-A heatsink)

To remove the CPU and heatsink:

- 1. Remove the rear cover. For more information, refer to Chassis cover.
- 2. Remove the air ducts. For more information, refer to Air duct(s).
- 3. Push the lock latches inwards on all four corners of the heatsink, then slightly twist each of the heatsink screws counterclockwise in the order shown on the illustration to loosen the heatsink.



Intel[®] recommends a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



4. Completely loosen all the screws on the heatsink, then lift and remove it from the motherboard.



Intel[®] recommends a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



5. Flip the heatsink over to reveal the CPU and carrier bracket. Flip the carrier lever over to release the CPU, then remove the CPU from the heatsink and carrier assembly.



6. Remove the carrier bracket from the heatsink.



2.3.2 Removing the CPU and heatsink (for Type-B heatsink)

To remove the CPU and heatsink:

- 1. Remove the rear cover. For more information, refer to Chassis cover.
- 2. Remove the air ducts. For more information, refer to Air ducts.
- Push the lock latches inwards on all four corners of the heatsink, and then slightly twist each of the heatsink screws counterclockwise in the order shown on the illustration to loosen the heatsink.



Intel[®] recommends a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



4. Completely loosen all the screws on the heatsink in the order shown, and then lift and remove it from the motherboard.



Intel[®] recommends a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



5. Flip the heatsink over to reveal the CPU and carrier bracket. Flip the carrier lever over to release the CPU, then remove the CPU from the heatsink and carrier assembly.



6. Remove the carrier bracket from the heatsink.


2.3.3 Installing the CPU and heatsink (for Type-A heatsink)

To install the CPU and heatsink:

- 1. Remove the air duct. For more information, refer to the Removing the air duct section
- 2. Locate the CPU sockets on your motherboard.



Z13PP-D32 CPU TR4 Socket

3. Attach the CPU to the carrier bracket, ensure the triangle mark is on the same side as the bracket lever, and then attach the CPU and carrier to the heatsink.



4. Remove the PNP cap from the CPU socket.

- 5. Align the heatsink and CPU assembly to the CPU socket, then place the heatsink on top of the CPU socket (A). Push the lock latches outwards on all four corners of the heatsink so that the heatsink and CPU assembly is secured to the CPU socket (B).
 - Ensure the triangle mark on the CPU is located in the same corner as the CPU socket.



6. Do two (2) clockwise turns on each of the heatsink screws in the cross order pattern shown on the illustration until the heatsink screws are tightened and the heatsink is secured onto the motherboard.



Intel® recommends a using a torque driver with a T-30 bit and a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



7. Replace the air duct. For more information, refer to the **Replacing the air duct** section.



2.3.4 Installing the CPU and heatsink (for Type-B heatsink)

To install the CPU and heatsink:

- 1. Remove the air duct. For more information, refer to the Removing the air duct section
- 2. Locate the CPU sockets on your motherboard.



Z13PP-D32 CPU TR4 Socket

3. Attach the CPU to the carrier bracket, ensure the triangle mark is on the same side as the bracket lever, then attach the CPU and carrier to the heatsink.



4. Remove the PNP cap from the CPU socket.

 Align the heatsink and CPU assembly to the CPU socket, and then place the heatsink on top of the CPU socket (A). Push the lock latches outwards on all four corners of the heatsink so that the heatsink and CPU assembly is secured to the CPU socket (B).



6. Do two (2) clockwise turns on each of the heatsink screws in the cross order pattern shown on the illustration until the heatsink screws are tightened.



Intel[®] recommends a using a torque driver with a T-30 bit and a torque value of 8 lbf-in to prolong the longetivity of all PEEK nuts after the quality of the load post is corrected.



7. Tighten the remaining heatsink screws to secure the heatsink to the motherboard.



Intel[®] recommends a torque value of 8 lbf-in to prolong the longevity of all PEEK nuts after the quality of the load post is corrected.



8. Replace the air duct. For more information, refer to the **Replacing the air duct** section.

2.4 System memory

2.4.1 Overview

The motherboard comes with 32 Double Data Rate 5 (DDR5) Dual Inline Memory Modules (DIMM) sockets.

The figure illustrates the location of the DDR5 DIMM sockets:



2.4.2 Memory Configurations

You may install 16GB, 32GB, and 64GB RDIMMs or 64GB, 128GB, 256GB 3DS / 3DS RDIMM into the DIMM sockets. If you are not sure on which slots to install the DIMMs, you can use the recommended memory configuration in this section for reference.



- Refer to ASUS Server AVL for the updated list of compatible DIMMs.
- Always install DIMMs with the same CAS latency. For optimum compatibility, it is
 recommended that you obtain memory modules from the same vendor.
- Start installing the DIMMs into the second slots (such as CPU1_DIMM_A1 and CPU2_ DIMM_A1)

Recommended memory configuration for 1 CPU Configuration

| 1 CPU Configuration (must be on CPU1) | | | | | | | | | | | |
|---------------------------------------|---------|---------|---------|---------|---------|---------|---------|---------|--|--|--|
| | DIMM_A2 | DIMM_A1 | DIMM_B2 | DIMM_B1 | DIMM_C2 | DIMM_C1 | DIMM_D2 | DIMM_D1 | | | |
| 1 DIMM | | • | | | | | | | | | |
| 2 DIMMs | | • | | | | | | | | | |
| 4 DIMMs | | • | | | | • | | | | | |
| 6 DIMMs | | • | | | | • | | • | | | |
| 8 DIMMs | | • | | • | | • | | • | | | |
| 12 DIMMs | • | • | | • | • | • | | • | | | |
| 16 DIMMs | • | • | • | • | • | • | • | • | | | |

| 1 CPU Co | 1 CPU Configuration (must be on CPU1) | | | | | | | | | | | |
|----------|---------------------------------------|---------|---------|---------|---------|---------|---------|---------|--|--|--|--|
| | DIMM_H1 | DIMM_H2 | DIMM_G1 | DIMM_G2 | DIMM_F1 | DIMM_F2 | DIMM_E1 | DIMM_E2 | | | | |
| 1 DIMM | | | | | | | | | | | | |
| 2 DIMMs | | | • | | | | | | | | | |
| 4 DIMMs | | | • | | | | • | | | | | |
| 6 DIMMs | | | • | | • | | • | | | | | |
| 8 DIMMs | • | | • | | • | | • | | | | | |
| 12 DIMMs | • | | • | • | • | | • | • | | | | |
| 16 DIMMs | • | • | • | • | • | • | • | • | | | | |

| Dual CPU configurati | on | | | | | | | | | |
|----------------------|----|---|---|----|----|----|----|--|--|--|
| DIMMs | | | | | | | | | | |
| | 2 | 4 | 8 | 12 | 16 | 24 | 32 | | | |
| CPU1_DIMM_A2 | | 1 | | | | • | • | | | |
| CPU1_DIMM_A1 | • | • | • | • | • | • | • | | | |
| CPU1_DIMM_B2 | | | | | | | • | | | |
| CPU1_DIMM_B1 | | | | | • | • | • | | | |
| CPU1_DIMM_C2 | | | | | | • | • | | | |
| CPU1_DIMM_C1 | | | • | • | • | • | • | | | |
| CPU1_DIMM_D2 | | | | | | | • | | | |
| CPU1_DIMM_D1 | | | | • | • | • | • | | | |
| CPU1_DIMM_H1 | | | | | • | • | • | | | |
| CPU1_DIMM_H2 | | | | | | | • | | | |
| CPU1_DIMM_G1 | | • | • | • | • | • | • | | | |
| CPU1_DIMM_G2 | | | | | | • | • | | | |
| CPU1_DIMM_F1 | | | | • | • | • | • | | | |
| CPU1_DIMM_F2 | | | | | | | • | | | |
| CPU1_DIMM_E1 | | | • | • | • | • | • | | | |
| CPU1_DIMM_E2 | | | | | | • | • | | | |
| CPU2_DIMM_A2 | | | | | | • | • | | | |
| CPU2_DIMM_A1 | • | • | • | • | • | • | • | | | |
| CPU2_DIMM_B2 | | | | | | | • | | | |
| CPU2_DIMM_B1 | | | | | • | • | • | | | |
| CPU2_DIMM_C2 | | | | | | • | • | | | |
| CPU2_DIMM_C1 | | | • | • | • | • | • | | | |
| CPU2_DIMM_D2 | | | | | | | • | | | |
| CPU2_DIMM_D1 | | | | • | • | • | • | | | |
| CPU2_DIMM_H1 | | | | | • | • | • | | | |
| CPU2_DIMM_H2 | | | | | | | • | | | |
| CPU2_DIMM_G1 | | • | • | • | • | • | • | | | |
| CPU2_DIMM_G2 | | | | | | • | • | | | |
| CPU2_DIMM_F1 | | | | • | • | • | • | | | |
| CPU2_DIMM_F2 | | | | | | | • | | | |
| CPU2_DIMM_E1 | | | • | • | • | • | • | | | |
| CPU2_DIMM_E2 | | | | | | • | • | | | |

Recommended memory configuration for 2 CPU Configuration

HBM SKU memory population table

| 1 CPU Co | onfiguratio | on (must b | be on CPU | 11) | | | | |
|----------|-------------|------------|-----------|---------|---------|---------|---------|---------|
| | DIMM_A2 | DIMM_A1 | DIMM_B2 | DIMM_B1 | DIMM_C2 | DIMM_C1 | DIMM_D2 | DIMM_D1 |
| 1 DIMM | | • | | | | | | |
| 2 DIMMs | | • | | | | | | |
| 4 DIMMs | | • | | | | • | | |
| 8 DIMMs | | • | | • | | • | | • |
| 16 DIMMs | • | • | • | • | • | • | • | • |
| | | | | | | | | |
| 1 CPU Co | onfiguratio | on (must k | be on CPU | 1) | | | | |
| | DIMM_H1 | DIMM_H2 | DIMM_G1 | DIMM_G2 | DIMM_F1 | DIMM_F2 | DIMM_E1 | DIMM_E2 |
| 1 DIMM | | | | | | | | |
| 2 DIMMs | | | • | | | | | |

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Recommended memory configuration for 1 CPU Configuration

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| | ASUS | RS70 | 0-E11 | Series |
|--|------|-------------|-------|--------|
|--|------|-------------|-------|--------|

4 DIMMs

8 DIMMs

16 DIMMs

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| Dual CPU configuration | on (for HE | вм ѕки с | Only) | | | | | | | |
|------------------------|------------|----------|-------|----|----|--|--|--|--|--|
| DIMMs | | | | | | | | | | |
| | 2 | 4 | 8 | 16 | 32 | | | | | |
| CPU1_DIMM_A2 | | | | | • | | | | | |
| CPU1_DIMM_A1 | • | • | • | • | • | | | | | |
| CPU1_DIMM_B2 | | | | | • | | | | | |
| CPU1_DIMM_B1 | | | | • | • | | | | | |
| CPU1_DIMM_C2 | | | | | • | | | | | |
| CPU1_DIMM_C1 | | | • | • | • | | | | | |
| CPU1_DIMM_D2 | | | | | • | | | | | |
| CPU1_DIMM_D1 | | | | • | • | | | | | |
| CPU1_DIMM_H1 | | | | • | • | | | | | |
| CPU1_DIMM_H2 | | | | | • | | | | | |
| CPU1_DIMM_G1 | | • | • | • | • | | | | | |
| CPU1_DIMM_G2 | | | | | • | | | | | |
| CPU1_DIMM_F1 | | | | • | • | | | | | |
| CPU1_DIMM_F2 | | | | | • | | | | | |
| CPU1_DIMM_E1 | | | • | • | • | | | | | |
| CPU1_DIMM_E2 | | | | | • | | | | | |
| CPU2_DIMM_A2 | | | | | • | | | | | |
| CPU2_DIMM_A1 | • | • | • | • | • | | | | | |
| CPU2_DIMM_B2 | | | | | • | | | | | |
| CPU2_DIMM_B1 | | | | • | • | | | | | |
| CPU2_DIMM_C2 | | | | | • | | | | | |
| CPU2_DIMM_C1 | | | • | • | • | | | | | |
| CPU2_DIMM_D2 | | | | | • | | | | | |
| CPU2_DIMM_D1 | | | | • | • | | | | | |
| CPU2_DIMM_H1 | | | | • | • | | | | | |
| CPU2_DIMM_H2 | | | | | • | | | | | |
| CPU2_DIMM_G1 | | • | • | • | • | | | | | |
| CPU2_DIMM_G2 | | | | | • | | | | | |
| CPU2_DIMM_F1 | | | | • | • | | | | | |
| CPU2_DIMM_F2 | | | | | • | | | | | |
| CPU2_DIMM_E1 | | | • | • | • | | | | | |
| CPU2_DIMM_E2 | | | | | • | | | | | |

Recommended memory configuration for 2 CPU Configuration

If you wish to install PMem as well, refer to the following tables for configurations

| 1 CPU Configuration (must be on CPU1) | | | | | | | | | | | |
|---------------------------------------|---------|---------|---------|---------|---------|---------|---------|---------|--|--|--|
| Channel | 1 | 4 | В | | С | | D | | | | |
| DDR5+CPS | DIMM_A2 | DIMM_A1 | DIMM_B2 | DIMM_B1 | DIMM_C2 | DIMM_C1 | DIMM_D2 | DIMM_D1 | | | |
| 4+4 | | CPS | | DDR5 | | CPS | | DDR5 | | | |
| 6+1 | | DDR5 | | CPS | | DDR5 | | DDR5 | | | |
| 8+1 | | DDR5 | | DDR5 | | DDR5 | CPS | DDR5 | | | |
| 0.4 | CPS | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 | | | |
| 0+4 | | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 | | | |
| 8+8 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | | | |

Recommended memory configuration for 1 CPU Configuration

| 1 CPU Configuration (must be on CPU1) | | | | | | | | | | | |
|---------------------------------------|---------|---------|---------|---------|---------|---------|---------|---------|--|--|--|
| Channel | H | Н | | G | | F | | E | | | |
| DDR5+CPS | DIMM_H1 | DIMM_H2 | DIMM_G1 | DIMM_G2 | DIMM_F1 | DIMM_F2 | DIMM_E1 | DIMM_E2 | | | |
| 4+4 | DDR5 | | CPS | | DDR5 | | CPS | | | | |
| 6+1 | | | DDR5 | | DDR5 | | DDR5 | | | | |
| 8+1 | DDR5 | | DDR5 | | DDR5 | | DDR5 | | | | |
| 0.4 | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 | CPS | | | |
| 0+4 | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 | | | | |
| 8+8 | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | | | |

| Channel | A (C | PU1) | B (C | PU1) | C (C | PU1) | D (C | PU1) |
|----------|---------|---------|---------|---------|---------|---------|---------|---------|
| DDR5+CPS | DIMM_A2 | DIMM_A1 | DIMM_B2 | DIMM_B1 | DIMM_C2 | DIMM_C1 | DIMM_D2 | DIMM_D1 |
| 4+4 | | CPS | | DDR5 | | CPS | | DDR5 |
| 6+1 | | DDR5 | | CPS | | DDR5 | | DDR5 |
| 8+1 | | DDR5 | | DDR5 | | DDR5 | CPS | DDR5 |
| 0.4 | CPS | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 |
| 0+4 | | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 |
| 8+8 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | |

Recommended memory configuration for 2 CPU Configuration

| Channel | H (C | PU1) | G (C | PU1) | F (C | PU1) | E (C | PU1) |
|----------|---------|---------|---------|---------|---------|---------|---------|---------|
| DDR5+CPS | DIMM_H1 | DIMM_H2 | DIMM_G1 | DIMM_G2 | DIMM_F1 | DIMM_F2 | DIMM_E1 | DIMM_E2 |
| 4+4 | DDR5 | | CPS | | DDR5 | | CPS | |
| 6+1 | | | DDR5 | | DDR5 | | DDR5 | |
| 8+1 | DDR5 | | DDR5 | | DDR5 | | DDR5 | |
| 0.4 | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 | CPS |
| 0+4 | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 | |
| 8+8 | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS |

| Channel | A (CPU2) | | B (CPU2) | | C (CPU2) | | D (CPU2) | |
|----------|----------|---------|----------|---------|----------|---------|----------|---------|
| DDR5+CPS | DIMM_A2 | DIMM_A1 | DIMM_B2 | DIMM_B1 | DIMM_C2 | DIMM_C1 | DIMM_D2 | DIMM_D1 |
| 4+4 | | CPS | | DDR5 | | CPS | | DDR5 |
| 6+1 | | DDR5 | | CPS | | DDR5 | | DDR5 |
| 8+1 | | DDR5 | | DDR5 | | DDR5 | CPS | DDR5 |
| 0.4 | CPS | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 |
| 0+4 | | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 |
| 8+8 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 |

| Channel | H (C | PU2) | G (C | PU2) | F (C | PU2) | E (C | PU2) |
|----------|---------|---------|---------|---------|---------|---------|---------|---------|
| DDR5+CPS | DIMM_H1 | DIMM_H2 | DIMM_G1 | DIMM_G2 | DIMM_F1 | DIMM_F2 | DIMM_E1 | DIMM_E2 |
| 4+4 | DDR5 | | CPS | | DDR5 | | CPS | |
| 6+1 | | | DDR5 | | DDR5 | | DDR5 | |
| 8+1 | DDR5 | | DDR5 | | DDR5 | | DDR5 | |
| 0.4 | DDR5 | | DDR5 | CPS | DDR5 | | DDR5 | CPS |
| 0+4 | DDR5 | CPS | DDR5 | | DDR5 | CPS | DDR5 | |
| 8+8 | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS | DDR5 | CPS |

2.4.3 Installing a DIMM



Ensure to unplug the power supply before adding or removing DIMMs or other system components. Failure to do so may cause severe damage to both the motherboard and the components.

- 1. Unlock a DIMM socket by pressing the retaining clips outward.
- Align a DIMM on the socket such that the notch on the DIMM matches the DIMM slot key on the socket.





A DIMM is keyed with a notch so that it fits in only one direction. DO NOT force a DIMM into a socket in the wrong direction to avoid damaging the DIMM.

 Hold the DIMM by both of its ends then insert the DIMM vertically into the socket. Apply force to both ends of the DIMM simultaneously until the retaining clips snaps back into place.

Ensure that the DIMM is sitting firmly on the DIMM slot.



Always insert the DIMM into the socket VERTICALLY to prevent DIMM notch damage.

2.4.4 Removing a DIMM

- 1. Remove the chassis cover. For more information, see the section Chassis cover.
- 2. Simultaneously press the retaining clips outward to unlock the DIMM.
- 3. Remove the DIMM from the socket.





Support the DIMM lightly with your fingers when pressing the retaining clips. The DIMM might get damaged when it flips out with extra force.

2.5 (optional) Front bezel

For extra security, a front bezel (purchased separately) can be installed to prevent unauthorized physical access to the hard drives and power button.

2.5.1 Removing the front bezel

1. Push the bezel release latch on the front bezel towards the right to unlock the bezel (A) and pull the left side of the bezel slightly away from the system (B).



2. Slide the front bezel to the left to detach the front bezel, then remove it from the system.



2.5.2 Installing the front bezel

1. Align the two (2) right notches on the front bezel to the notch holes on the right side of the front panel.



2. Push the bezel into the system until it clicks into place (A), and then slide the bezel release latch to the left to lock the bezel to the system (B).





Make sure the bezel release latch is in the unlock state (pushed to the right) before attaching the bezel to the front panel.

2.6 Storage devices

The system supports twelve (12) 2.5" hot-swap SATA/SAS/NVMe storage devices (up to 8 x NVMe/SAS/SATA + 4 x NVMe/SATA). Storage devices installed on the storage device tray connect to the motherboard SATA/SAS/NVMe ports via the SATA/SAS/NVMe backplane.



• All bays support 2.5" drives with trays.

2.6.1 Installing a 2.5" hot-swap SATA/SAS/NVMe storage device

1. Press the spring lock.







 Place the 2.5" storage device into the storage device tray then secure it with four screws.





clicks and secures the storage device tray in place.

Push the storage device tray and HDD

assembly all the way into the depth of the bay until the tray lever and spring lock

Ø

4.

- When installed, the SATA/SAS/NVMe connector on the storage device connects to the SATA/SAS/NVMe interface on the backplane.
- The storage device tray is correctly placed when its front edge aligns with the bay edge.
- 5. Repeat steps 1 to 4 to install the other 2.5" storage devices.

2.7 Expansion slots

The barebone server comes with two PCIe slots (A) and (B). These slots are pre-installed with a riser card bracket and a butterfly riser card bracket for installing PCIe expansion cards. You need to remove these expansion card brackets if you want to install PCIe expansion cards.





Riser card bracket 1

Riser card bracket 1 supports PCIe Gen5 slots RS4_PCIE1 (top) and RS4_PCIE2 (bottom).

| | Operation mode | | |
|-------------|----------------|--|--|
| PCIE SIOT A | Mode 1 | Mode 2 | |
| RS4_PCIE1 | x16 | x16 | |
| RS4_PCIE2 | x16 | N/A (if an OCP 3.0 card is installed) | |

Riser card bracket 2 (butterfly riser card bracket)

Riser card bracket 2 supports one rear low profile PCIe Gen5 x16 slot and one internal low profile PCIe Gen5 x16 slot.

| PCIe slot B | Operation mode |
|-------------------------|----------------|
| RS2_LAN_PCIE1 | x8 (Gen 4) |
| RS2_PCIE1 | x16 |
| RS3_PCIE1 (Internal) | x8 |

2.7.1 Installing an expansion card to the PCIe riser card bracket

The PCIe riser card bracket that is pre-installed on PCIe slot A has two PCIe x16 slots. The two PCIe x16 slots provide x16 Gen5 links, with the top PCIe slot's signal provided from CPU1 and the bottom PCIe slot's signal from CPU1.

To install PCIe x16 (Gen5 x16 link) proprietary cards, such as a graphics card, to the PCIe riser card bracket:

- 1. Remove the butterfly riser card bracket installed on PCIe slot B. For more information, refer to **Installing an expansion card to the butterfly riser card bracket**.
- 2. Lift the PCIe riser card bracket out of the chassis by firmly holding the tab with one hand and slipping your other hand under the opposite end of the bracket and then pulling upwards to detach it from PCIe slot A on the motherboard.



- 3. Prepare your expansion cards and flip the PCIe riser card bracket over.
- 4. Flip the metal bracket lock open (A), and then slide the two metal brackets out of the PCIe riser card bracket (B).



 (optional) Install the GPU back bracket to the location shown in the illustration below using two (2) screws.



6. Install your expansion cards to the PCIe slots on the PCIe riser card bracket. The illustration below is an example of a graphics card.



7. Once your expansion cards are installed, flip the metal bracket lock back to secure the expansion cards to the PCIe riser card bracket.



 Align the PCIe riser card bracket to the notch holes on the chassis and PCIe slot A on the motherboard, and then push the PCIe riser card bracket down until it is seated firmly in the chassis.



Make sure that no cables are below or in the way of the PCIe riser card bracket when installing it to the chassis.



9. Replace the butterfly riser card bracket. For more information please refer to **Installing** an expansion card to the butterfly riser card bracket.

2.7.2 Installing an OCP card

To install an OCP card to the PCIe riser card bracket:

- 1. Insert the OCP 3.0 card into the OCP 3.0 slot from the rear of the system.
- 2. Make sure the OCP 3.0 card is seated securely in the OCP 3.0 slot, and then secure it using the thumbscrew.



2.7.3 Installing an expansion card to the butterfly riser card bracket

The pre-installed butterfly riser card bracket on PCIe slot B supports Low Profile (LP), Half-Length (HL) PCIe x16 expansion cards.

To install a PCIe x16 (Gen5 x16 link) expansion card on the butterfly riser card bracket:

1. Lift the butterfly riser out of the chassis by firmly holding it by the tab and pulling it upwards to detach it from the PCIE x16 slot on the motherboard.



2. Flip the butterfly riser card bracket over, and then flip the metal bracket lock open (A) to remove the metal bracket (B).



3. Install the expansion card to the **RS2_PCIE1** slot on the butterfly riser card bracket (A), and then flip the metal bracket lock back to secure the card (B).



4. Align the butterfly riser card bracket to PCIe slot B on the motherboard and push down until the butterfly riser card bracket is seated securely in the chassis.



2.7.4 Installing an ethernet expansion card to the butterfly riser card bracket

The pre-installed butterfly riser card bracket can support a 4-port Intel[®] I350-AM4 1G LAN controller expansion card or 2-port Intel[®] X710-AT2 Gigabit 10G LAN controller expansion card.



Do not install a 4-port ${\rm Intel}^{\otimes}$ I350-AM4 1G LAN controller expansion card if you plan to install an external rear fan.

To install a 4-port Intel[®] I350-AM4 1G LAN controller expansion card or 2-port Intel[®] X710-AT2 Gigabit 10G LAN controller expansion card on the butterfly riser card bracket:

- 1. Follow step 1 of **Installing an expansion card to the butterfly riser card bracket** to remove the butterfly riser card bracket from the chassis.
- Flip the butterfly riser card bracket over and insert the 4-port Intel[®] I350-AM4 1G LAN controller expansion card or 2-port Intel[®] X710-AT2 Gigabit 10G LAN controller expansion card to the RS2_LAN_PCIE1 slot (A) on the butterfly riser card bracket, then secure it using two (2) screws (B).



3. Follow step 4-5 of **Installing an expansion card to the butterfly riser card bracket** to install the butterfly riser card bracket to the chassis.

2.7.5 Installing an HBA/RAID card to the butterfly riser card bracket

- 1. Prepare the HBA/RAID card.
- 2. Remove the two (2) screws on the HBA/RAID card (A), and then remove the card bracket (B).



- 3. Follow step 1 of **Installing an expansion card to the butterfly riser card bracket**. to remove the butterfly riser card bracket from the chassis.
- 4. Flip the butterfly riser card bracket over and insert the HBA/RAID card into the internal **RS3_PCIE1** slot (A), and then secure it using two (2) screws (B).



- 5. Follow step 4-5 of **Installing an expansion card to the butterfly riser card bracket** to install the butterfly riser card bracket to the chassis.
- Connect the MCIO/miniSAS cables from the HBA/RAID card (A) to the NVMe/SATA/ SAS backplane (B). Refer to the section NVMe/SATA/SAS backplane for the locations of the MCIO connectors.





- The illustration above is for reference only.
- For more information or assistance, please refer to <u>www.asus.com</u>.

To remove an HBA/RAID card:

- 1. Follow step 1 of **Installing an expansion card to the butterfly riser card bracket** to remove the butterfly riser card bracket from the chassis.
- 2. Disconnect the cables from the HBA/RAID card.



3. Remove the two (2) screws securing the card to the butterfly riser card bracket (A), and then remove the HBA/RAID card (B).



2.7.6 Installing the Cache Vault Power Module

The cache vault power module is required for selected HBA/RAID card models. You may install the cache vault power module to the butterfly riser card bracket. Please refer to the steps below to install the cache vault power module to your server system.

To install the cache vault power module:

- 1. Follow steps 1 and 2 of the **Installing an expansion card to the butterfly riser card bracket** section to remove the butterfly riser card bracket from the chassis.
- 2. Align the three screw holes on the Cache Vault Power Module clip with the three screw holes on the riser bracket, then secure the clip with the three bundled screws and hex nuts.



3. Align and install the Cache Vault Power Module into the Cache Vault Power Module clip.



- 4. Connect the Cache Vault Power Module to the S-CAP connector on the HBA RAID card.
- 5. Install the butterfly riser card bracket back into the PCIe slot on the motherboard. Make sure that the gold fingers on the butterfly riser card bracket is firmly seated in place.

2.7.7 Installing an M.2 (NGFF) card

You may install M.2 cards (supports 2260, 2280) to the onboard M.2 (NGFF) slots on the motherboard.

- 1. Remove the riser card bracket. Please refer to **Installing an expansion card to the butterfly riser card bracket** for more information.
- 2. Locate the M.2 connectors (NGFF1 / NGFF2) on the motherboard.



Z13PP-D32 NGFF connectors

- 3. Select the appropriate screw hole on the motherboard for your M.2 card, then secure the bundled standoff to the motherboard.
- Insert the M.2 card into the M.2 (NGFF) slot, and then secure the card using the bundled screw(s).



5. Repeat steps 3 and 4, if you have another M.2 card to install.

2.7.8 Configuring an expansion card

After installing an expansion card, configure it by adjusting the software settings.

- 1. Turn on the system and change the necessary BIOS settings, if any. See the BIOS Setup chapter information on BIOS setup.
- 2. Assign an IRQ to the card. Refer to the following tables.
- 3. Install the software drivers for the expansion card.

Standard Interrupt assignments

| IRQ | Priority | Standard function |
|-----|----------|-----------------------------|
| 0 | 1 | System Timer |
| 1 | 2 | Keyboard Controller |
| 2 | - | Programmable Interrupt |
| 3* | 11 | Communications Port (COM2) |
| 4* | 12 | Communications Port (COM1) |
| 5* | 13 | |
| 6 | 14 | Floppy Disk Controller |
| 7* | 15 | |
| 8 | 3 | System CMOS/Real Time Clock |
| 9* | 4 | ACPI Mode when used |
| 10* | 5 | IRQ Holder for PCI Steering |
| 11* | 6 | IRQ Holder for PCI Steering |
| 12* | 7 | PS/2 Compatible Mouse Port |
| 13 | 8 | Numeric Data Processor |
| 14* | 9 | Primary IDE Channel |
| 15* | 10 | Secondary IDE Channel |

* These IRQs are usually available for ISA or PCI devices.

2.8 Cable connections



The bundled system cables are pre-connected before shipment. You do not need to disconnect these cables unless you are going to remove pre-installed components to install additional devices.

 Refer to the Motherboard Information chapter for detailed information on the connectors.



Pre-connected system cables

- 1. 8-pin BPPWR2 and BPPWR3 power connectors (connected to backplane)
- 2. Panel connector (connected to front I/O board)
- 3. System fan connectors
- MCIOPCIE5_P2, MCIOPCIE6_P2, MCIOPCIE7_P2, and MCIOPCIE8_P2 MCIO PCIe connectors (connected to backplane, supported by CPU2)
- 5. MCIOPCIE7_P1, and MCIOPCIE8_P1 MCIO PCIe connectors (supported by CPU1, only connected if an optional OCP slot is pre-installed)
- MCIOPCIE5_P1, MCIOPCIE6_P1 MCIO PCIe connectors (connected to backplane, supported by CPU1)
- 7. SLIMSATA1 and SLIMSSATA1 connectors

2.9 Backplane cabling



2.10 Storage device configuration and cabling

This section illustrates some storage configurations that are recommended for your server system. Before you start installing or removing the storage device cables, ensure that you have installed the correct storage devices into the supported bays.





- Bays 1 to 8 support NVMe/SATA/SAS. SAS support requires an optional HBA/RAID card.
- Bays 9 to 12 support NVMe/SATA.
- All bays support 2.5" drives with trays.

2.10.1 12 x SATA storage device configuration and cabling



- The illustrations in this section are for reference only and may vary between models.
- You may still support 4, 8 or 12 NVMe bays with this configuration, for more information please refer to the following sections:
 - 4 NVMe support: 4 x NVMe storage device configuration and cabling
 - 8 NVMe support: 8 x NVMe storage device configuration and cabling
 - 12 NVMe support: 12 x NVMe storage device configuration and cabling

| Backplane connector | Cable | Connect to |
|---------------------|---|---------------------------|
| SLIMSAS1 | SlimSAS (vertical) to SlimSAS (vertical) | SLIMSATA1 on motherboard |
| SLIMSAS2 | SlimSAS (vertical) to SlimSAS (vertical) | SLIMSATA1 on motherboard |
| SLIMSAS3 | SlimSAS (vertical) to SlimSAS (vertical) | SLIMSSATA1 on motherboard |

1. Install the storage devices into the supported bays.



2. Remove the rear and backplane covers. For more information, refer to **Removing the rear cover** and **Removing the backplane cover**.

3. Locate the backplane (A), and then cut the cable tie(s) (B).



4. Connect the SlimSAS cables to the motherboard and the backplane.


5. Tie the cables with cable tie(s) (A), then reinstall the backplane cover to the chassis (B).



2.10.2 4 x NVMe storage device configuration and cabling



- The illustrations in this section are for reference only and may vary between models.
- You may still support either SATA for bays 1 to 12 or SAS for bays 1 to 8 with this configuration, for more information please refer to the following sections:
 - SATA support for bays 1 to 12: 12 x SATA storage device configuration and cabling
 - SAS support for bays 1 to 8 and SATA support for bays 9 to 12: 8 x SAS and 4 x SATA storage device configuration and cabling

| Backplane connector | Cable | Connect to |
|---------------------|--------------|-----------------------------|
| MCIO_P1 | MCIO to MCIO | MCIOPCIE5_P1 on motherboard |
| MCIO_P2 | MCIO to MCIO | MCIOPCIE6_P1 on motherboard |

1. Install the storage devices into the supported bays.

Refer to section Storage Devices for details on how to install storage devices.

| | NVMe | NVMe | |
|---|----------------|--------------------|-------------------------------|
| | | | |
| 0 | Í 00000000000€ | 000000000000000000 | |
| 0 | | |) 💿 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 🕄 |
| | | | |
| | NVMe | NVMe | |

- 2. Remove the rear and backplane covers. For more information, refer to **Removing the rear cover** and **Removing the backplane cover**.
- 3. Locate the backplane (A), and then cut the cable tie(s) (B).



4. Connect the MCIO cables to the motherboard and the backplane.



5. Tie the cables with cable tie(s) (A), then reinstall the backplane cover to the chassis (B).



2.10.3 8 x NVMe storage device configuration and cabling



- The illustrations in this section are for reference only and may vary between models.
- You may still support either SATA for bays 1 to 12 or SAS for bays 1 to 8 with this configuration, for more information please refer to the following sections:
 - SATA support for bays 1 to 12: 12 x SATA storage device configuration and cabling
 - SAS support for bays 1 to 8 and SATA support for bays 9 to 12: 8 x SAS and 4 x SATA storage device configuration and cabling

| Backplane connector | Cable | Connect to |
|---------------------|--------------|-----------------------------|
| MCIO_P1 | MCIO to MCIO | MCIOPCIE5_P1 on motherboard |
| MCIO_P2 | MCIO to MCIO | MCIOPCIE6_P1 on motherboard |
| MCIO_P3 | MCIO to MCIO | MCIOPCIE7_P2 on motherboard |
| MCIO_P4 | MCIO to MCIO | MCIOPCIE8_P2 on motherboard |

1. Install the storage devices into the supported bays.



2. Remove the rear and backplane covers. For more information, refer to **Removing the rear cover** and **Removing the backplane cover**.

3. Locate the backplane (A), and then cut the cable tie(s) (B).







5. Tie the cables with cable tie(s) (A), then reinstall the backplane cover to the chassis (B).



2.10.4 12 x NVMe storage device configuration and cabling



- The illustrations in this section are for reference only and may vary between models.
- You may still support one of the following configurations with this configuration, for more information please refer to the following sections:
 - SATA support for bays 1 to 12: 12 x SATA storage device configuration and cabling
 - SAS support for bays 1 to 8 and SATA support for bays 9 to 12: 8 x SAS and 4 x SATA storage device configuration and cabling

| Backplane connector | Cable | Connect to |
|---------------------|--------------|-----------------------------|
| MCIO_P1 | MCIO to MCIO | MCIOPCIE5_P1 on motherboard |
| MCIO_P2 | MCIO to MCIO | MCIOPCIE6_P1 on motherboard |
| MCIO_P3 | MCIO to MCIO | MCIOPCIE7_P2 on motherboard |
| MCIO_P4 | MCIO to MCIO | MCIOPCIE8_P2 on motherboard |
| MCIO_P5 | MCIO to MCIO | MCIOPCIE5_P2 on motherboard |
| MCIO_P6 | MCIO to MCIO | MCIOPCIE6_P2 on motherboard |

1. Install the storage devices into the supported bays.



2. Remove the rear and backplane covers. For more information, refer to **Removing the rear cover** and **Removing the backplane cover**.

3. Locate the backplane (A), and then cut the cable tie(s) (B).



4. Connect the MCIO cables to the motherboard and the backplane.



5. Tie the cables with cable tie(s) (A), then reinstall the backplane cover to the chassis (B).



2.10.5 8 x SAS and 4 x SATA storage device configuration and cabling



- The illustrations in this section are for reference only and may vary between models.
- You may still support 4, 6 or 12 NVMe bays with this configuration, for more information please refer to the following sections:
 - 4 NVMe support: 4 x NVMe storage device configuration and cabling
 - 8 NVMe support: 8 x NVMe storage device configuration and cabling
 - 12 NVMe support: 12 x NVMe storage device configuration and cabling

| Backplane connector | Cable | Connect to |
|---------------------|---|---------------------------|
| SLIMSAS1 | SlimSAS (vertical) to SlimSAS (vertical) | HBA/RAID Card |
| SLIMSAS2 | SlimSAS (vertical) to SlimSAS (vertical) | HBA/RAID Card |
| SLIMSAS3 | SlimSAS (vertical) to SlimSAS (vertical) | SLIMSSATA1 on motherboard |

1. Install the storage devices into the supported bays.



2. Install the HBA/RAID card to your system. For more information, refer to Installing an HBA/RAID card.

3. Locate the backplane (A), and then cut the cable tie(s) (B).





4. Connect the MCIO cables to the motherboard and the backplane.

5. Tie the cables with cable tie(s) (A), then reinstall the backplane cover to the chassis (B).



2.11 Removable/optional components

This section explains how to install optional components into the system and covers the following components:

- 1. System fans
- 2. Redundant power supply module



Ensure that the system is turned off before removing any components.



You may need to remove previously installed component or factory shipped components when installing optional components.

2.11.1 System fans

To uninstall the system fans:

- 1. Disconnect the system fan cable from the fan connector on the motherboard.
- 2. Lift the fan, then set it aside.



3. Repeat steps 1 to 2 to uninstall the other system fans.

To reinstall the system fans:

1. Insert the fan into the fan cage. The airflow directional arrow on the fan should point towards the system rear panel.



2. Connect the system fan cable to the fan connector on the motherboard.



To install the external rear fan (on selected models):



- We recommend installing the external fan when you have GPU cards installed.
- The external fan is only available with selected models.

1. Use a screwdriver to pry open the slot.



Do not install the 4-port ethernet expansion card if you wish to install the external rear fan.



- 2. Prepare the rear external fan.
- 3. Pass the cable on the rear external fan through the open slot.



4. Connect the cables on the rear external fan to the **REAR_FAN1** and **REAR_FAN2** connectors on the motherboard.



- 5. Align and place the rear external fan on the chassis.
- 6. Secure the rear external fan to the chassis with the thumbscrews.



2.11.2 Redundant power supply module

To replace a failed redundant power supply module:

1. Lift up the power supply module lever.



2. Hold the power supply module lever and press the PSU latch, then pull the power supply module out of the system chassis.



- 3. Prepare the replacement power supply module.
- Insert the replacement power supply module into the chassis then push it inwards until the latch locks into place.



2.12 Rail Kit Options

This server system supports the rail kit options listed below. For more information on rail kit installation, refer to corresponding documentation on the ASUS support site or on the official product site for this server system.



- We strongly recommend that at least two able-bodied persons perform the installation of the rail kit.
- We recommend the use of an appropriate lifting tool or device, if necessary.
- Friction rail kit
- 1m half extension ball bearing rail kit
- 1.2m half extension ball bearing rail kit
- 1U full extension ball bearing rail kit



Motherboard Information

This chapter includes the motherboard layout and brief descriptions of the jumpers and internal connectors.

3.1 Motherboard layout



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| 3. | USB 2.0 connector (10-1 pin USB78; USB2_1) | 3-16 |
| 4. | USB 3.2 Gen 1 connector (U31G1_34) | 3-16 |
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| 14. VPP_I2C connector (10-1 pin VPP_I2C1) | 3-22 |
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| 21. Platform Firmware Resilience (PFR) module connector (ROT_CON) | 3-26 |

3.2 Jumpers

1. Clear RTC RAM (3-pin CLRTC1)

This jumper allows you to clear the Real Time Clock (RTC) RAM in CMOS. You can clear the CMOS memory of date, time, and system setup parameters by erasing the CMOS RTC RAM data. The onboard button cell battery powers the RAM data in CMOS, which include system setup information such as system passwords.

To erase the RTC RAM:

- 1. Turn OFF the computer and unplug the power cord.
- 2. Move the jumper cap from pins 1–2 (default) to pins 2–3. Keep the cap on pins 2–3 for about 5–10 seconds, then move the cap back to pins 1–2.
- 3. Plug the power cord and turn ON the computer.
- Hold down the key during the boot process and enter BIOS setup to reenter data.



Except when clearing the RTC RAM, never remove the cap on CLRTC jumper default position. Removing the cap will cause system boot failure!



If the steps above do not help, remove the onboard battery and move the jumper again to clear the CMOS RTC RAM data. After the CMOS clearance, reinstall the battery.



Z13PP-D32 Clear RTC RAM

2. VGA Controller setting (3-pin VGA_SW1)

This jumper allows you to enable or disable the onboard VGA controller. Set to pins 1-2 to activate the VGA feature.



Z13PP-D32 VGA setting

3. Baseboard Management Controller setting (3-pin BMC_EN1)

This jumper allows you to enable (default) or disable on-board BMC. Ensure to set this BMC jumper to enabled to avoid system fan control and hardware monitor error.



Z13PP-D32 BMC setting

4. DMLAN setting (3-pin DM_IP_SEL1)

This jumper allows you to select the DMLAN setting. Set to pins 2-3 to force the DMLAN IP to static mode (IP=10.10.10.10, submask=255.255.255.0).



Z13PP-D32 DM_IP_SEL1 setting

5. IPMI SW setting (3-pin IPMI_SW1)

This jumper allows you to select which protocol in the GPU sensor to function.



Z13PP-D32 IPMI_SW1 setting

6. Smart Ride Through (SmaRT) setting (3-pin SMART_PSU1)

This jumper allows you to enable or disable the Smart Ride Through (SmaRT) function. This feature is enabled by default. Set to pins 2-3 to disable it. When enabled, SmaRT allows uninterrupted operation of the system during an AC loss event.



Z13PP-D32 Smart Ride Through setting

7. ME Firmware Force Recovery setting (3-pin ME_RCVR1)

This jumper allows you to quickly recover the Intel Management Engine (ME) firmware if it becomes corrupted.



Z13PP-D32 ME recovery setting

8. PCH_MFG1 setting (3-pin PCH_MFG1)

This jumper allows you to update the BIOS ME block select.



Z13PP-D32 PCH_MFG1 setting

9. LANNCSI setting (3-pin LANNCSI_SEL1)

This jumper allows you to select which LAN NCSI to enable.



10. CPLD setting (3-pin CPLD_UPDATE1)

This jumper allows you to enable or disable Complex Programmable Logic Devices (CPLD) updates.



Z13PP-D32 CPLD_UPDATE1 setting

11. MG INTR setting (3-pin MG_INTR1-3)

This jumper allows you to choose CPU1 PEHP ALERT# or CPU2 PEHP ALERT# interrupt from the system backplane.



Z13PP-D32 MG_INTR setting

12. RM Debug setting (3-pin RM_DEBUG1)

This jumper allows you to enable or disable BMC remote debug feature RM_DEBUG1 default is 1-2 (disable) and 2-3 is enable.



Z13PP-D32 RM_DEBUG1 setting

3.3 Internal LEDs

1. Standby Power LED (SBPWR1)

The motherboard comes with a standby power LED. The green LED lights up to indicate that the system is ON, in sleep mode, or in soft-off mode. This is a reminder that you should shut down the system and unplug the power cable before removing or plugging in any motherboard component. The illustration below shows the location of the onboard LED.



Z13PP-D32 Standby Power LED

2. Baseboard Management Controller LED (BMCLED1)

The BMC LED blinks to indicate that the on-board BMC is functional.



Z13PP-D32 BMC LED

3. Message LED (MESLED)

This onboard LED lights up red when there is a BMC event log generated.



4. Processor Catastrophic Error LED (CATERR1)

The Processor Catastrophic Error LED indicates that the system has experienced a fatal or catastrophic error and cannot continue to operate.



Z13PP-D32 CATERR1 LED

5. DIMM LED (DIMMLED1)

This LED blinks to indicate that there is 12 V power input to the memory.



6. ADR LED (ADRLED)

This LED blinks to indicate that the system is using asynchronous DRAM Refresh with Crow Pass memory modules.



3.4 Internal connectors

1. Slim SATA connector (SLIMSSATA1;SLIMSATA1)

This motherboard comes with Slim SATA connectors, the storage technology that supports Serial ATA. Each connector supports up to two devices.



Z13PP-D32 SLIMSATA connectors

2. MCIO PCIe connector (MCIOPCIE5-8_P1; MCIOPCIE5-8_P2)

Connects the PCIe signal to the riser card or NVMe port on the backplane.



3. USB 2.0 connector (10-1 pin USB78; Type-A USB2_1)

This connector is for USB 2.0 ports. Connect the USB module cable to the connector, and then install the module to a slot opening at the back of the system chassis. The USB connectors comply with USB 2.0 specification that supports up to 480 Mbps connection speed.





The USB port module is purchased separately.

4. USB 3.2 Gen 1 connector (U31G1_34)

This connector allows you to connect a USB 3.2 Gen 1 module for additional USB 3.2 Gen 1 ports on the front panel. The USB 3.2 Gen 1 connector provides data transfer speeds of up to 5 Gb/s. The Type-A connector allows you to directly connect a USB flash drive.



Z13PP-D32 USB 3.2 Gen 1 connector

5. Chassis Intrusion (2-pin INTRUSION1)

These leads are for the intrusion detection feature for chassis with intrusion sensor or microswitch. When you remove any chassis component, the sensor triggers and sends a high level signal to these leads to record a chassis intrusion event. The default setting is to short the CHASSIS# and the GND pin by a jumper cap to disable the function.



Z13PP-D32 Chassis Intrusion connector

6. Serial Port connector (10-1 pin COM1)

This connector is for a serial (COM) port. Connect the serial port module cable to this connector, then install the module to a slot opening at the back of the system chassis.



Z13PP-D32 Serial port connector

The COM module is purchased separately.

7. System Fan connectors (6-pin FRNT_FAN1-9; 6-pin REAR_FAN1-2)

The 8-pin FRNT_FAN connectors are connected to the Fan board and supports 9A per pin for the +12V pins. The 6-pin REAR_FAN connectors support 3A per pin for the +12V pins. Connect the fan cables to the fan connectors on the motherboard, making sure that the black wire of each cable matches the ground pin of the connector.

DO NOT forget to connect the fan cables to the fan connectors. Insufficient air flow inside

the system may damage the motherboard components. These are not jumpers! DO NOT place jumper caps on the fan connectors! \Lambda FRNT FAN1 B FRNT FAN2 2ª G 3ND NMM Shate SND -12V NSN N FRNT FAN8 🖸 FRNT FAN3 ŭo ****** GND PWM otate +12V 3ND otate -12V SND Q M FRNT FAN4 E FRNT FAN9 ***** e, 12V NMM SND SND SND -12V GND otate SND G FRNT FAN5 REAR FAN2 Z13PP-D32 FAN connectors ***** 12V WMM otate 12V SND ₽₹ tate 22 QN FRNT FAN6 G REAR FAN1 ***** ***** DNS NN 12V SND 12V ND FRNT FAN7 ***** 12V WM 12V 12V SND

8. TPM connector (14-1 pin TPM1)

This connector supports a Trusted Platform Module (TPM) system, which can securely store keys, digital certificates, passwords, and data. A TPM system also helps enhance network security, protects digital identities, and ensures platform integrity.



Chapter 3: Motherboard Information
9. M.2 (NGFF) connector (NGFF1-2)

These connectors allow you to install M.2 devices.



Z13PP-D32 NGFF connectors



This connector supports type 2260 / 2280 devices on PCIe interface.



The M.2 (NGFF) device is purchased separately

10. Backplane Power connector (8-pin BPPWR1-3)

These connectors are for the power supply plugs that connect to the backplane. The power supply plugs are designed to fit these connectors in only one orientation. Find the proper orientation and push down firmly until the connectors completely fit.



Z13PP-D32 BP power connectors



DO NOT connect VGA cards to these connectors. Doing so may cause system boot errors and permanent damage to your motherboard or device.

11. System Panel connector (10-1 pin SYS_PANEL1; 14-1 pin SYS_PANEL2)

This connector supports several chassis-mounted functions.



System power LED (FP_PLED)

This 1-pin connector is for the system power LED. Connect the chassis power LED cable to this connector. The system power LED lights up when you turn on the system power.

Message LED (FP_MLED)

This 2-pin connector is for the message LED cable that connects to the front message LED. The message LED is controlled by the BMC to indicate an abnormal event occurrence.

Locator LED connector (AUX_BMCLOCLED)

This connector allows you to connect the Locator LED. The Location LED helps visually locate and identify the server in error on a server rack.

Power Button/Soft-off Button connector (PWRBTN)

The 3-1 pin connector allows you to connect the system power button. Press the power button to power up the system, or put the system into sleep or soft-off mode (depending on the operating system settings).

Reset button connector (RESETBTN)

This connector allows you to connect the chassis-mounted reset button. Press the reset button to reboot the system.

TR1 Sensor connector (TR1 SENSOR)

This connector allows detection of the environmental temperature of the front panel.

Locator button connector (BMCLOCBTN#)

This connector allows you to connect the Locator button. Press the button to light up the Locator LED.

LAN LED connector (LAN1-4 LINK and Activity LED)

This 2-pin connector allows you to connect the Gigabit LAN Activity LED.

12. VGA connector (16-pin VGA_HDR1)

This connector supports the VGA High Dynamic-Range interface.



Z13PP-D32 Internal VGA connector

13. Micro SD card slot (MSD1)

Your motherboard supports SD Memory Card v2.00 (SDHC) / v3.00 (SDXC).





Disconnect all power (including redundant PSUs) from the existing system before you add or remove a Memory Card, then reboot the system to access the Memory Card.

14. VPP_I2C connector (10-1 pin VPP_I2C1)

The VPP_I2C connector is used for the storage backplane with sensor readings.



Z13PP-D32 VPP_I2C1 connector

15. BMC Debug UART connector (3-pin BMC_DEBUGUART1)

This connector is used for reading the BMC UART Debug log.



Z13PP-D32 BMC_DEBUGUART1 connector

16. CPLD JTAG connector (6-pin CPLD_JTAG1)

This connector is used for burning the CPLD JTAG.



Z13PP-D32 CPLD_JTAG1 connector

System Management Bus (SMBUS) connector (5-1 pin SMBUS2) 17.

This connector controls the system and power management-related tasks. This connector processes the messages to and from devices rather than tripping the individual control lines.



Z13PP-D32 SMBUS connector

18. VROC Key connector (4-pin VROC_KEY1)

The VROC (Virtual Raid on CPU) Key connector allows you to connect a VROC hardware key to enable additional CPU RAID functions with Intel® CPU RSTe.





The VROC hardware key is purchased separately.

19. OCP Side connector (24-pin OCP_SIDE1)

This connector connects the OCP 3.0 Riser card sideband signals to the motherboard



Z13PP-D32 OCP_SIDE1 connector

20. OCP Bus connector (OCP_BUS1)

This connector connects the OCP 3.0 Riser card NCSI signals to the motherboard



Z13PP-D32 OCP_BUS1

21. Platform Firmware Resilience (PFR) module connector (ROT_CON)

This connector allows you to connect a PFR module to enable platform firmware resilience functions.



Z13PP-D32 ROT_CON1 connector

4

BIOS Setup

This chapter tells how to change the system settings through the BIOS Setup menus. Detailed descriptions of the BIOS parameters are also provided.

4.1 Managing and updating your BIOS

The following utilities allow you to manage and update the motherboard Basic Input/Output System (BIOS) setup:

1. ASUS CrashFree BIOS 3

To recover the BIOS using a bootable USB flash disk drive when the BIOS file fails or gets corrupted.

2. ASUS EzFlash

Updates the BIOS using a USB flash disk.

Refer to the corresponding sections for details on these utilities.



Save a copy of the original motherboard BIOS file to a bootable USB flash disk drive in case you need to restore the BIOS in the future.

4.1.1 ASUS CrashFree BIOS 3 utility

The ASUS CrashFree BIOS 3 is an auto recovery tool that allows you to restore the BIOS file when it fails or gets corrupted during the updating process. You can update a corrupted BIOS file using a USB flash drive that contains the updated BIOS file.



Prepare a USB flash drive containing the updated motherboard BIOS before using this utility.

Recovering the BIOS from a USB flash drive

To recover the BIOS from a USB flash drive:

- 1. Insert the USB flash drive with the original or updated BIOS file to one USB port on the system.
- 2. The utility will automatically recover the BIOS. It resets the system when the BIOS recovery finished.



DO NOT shut down or reset the system while recovering the BIOS! Doing so would cause system boot failure!



The recovered BIOS may not be the latest BIOS version for this motherboard. Visit the ASUS website at www.asus.com to download the latest BIOS file.

4.1.2 ASUS EZ Flash Utility

The ASUS EZ Flash Utility feature allows you to update the BIOS without having to use a DOS-based utility.



Before you start using this utility, download the latest BIOS from the ASUS website at www.asus.com.

To update the BIOS using EZ Flash Utility:

- 1. Insert the USB flash disk that contains the latest BIOS file into the USB port.
- 2. Enter the BIOS setup program. Go to the **Tool** menu then select **Start ASUS EzFlash**. Press <Enter>.

| ASUSTek. EzFlash Utility | | | |
|--|--|--|--|
| Current Platform Platform : Z13PP-D32 Version : 0214 Build date: 05/03/2022 | New Platform Platform : Z13PP-D32 Version : 0214 Build date: 05/03/2022 | | |
| FS0 FS1 | | | |
| [Up/Down/Left/Right]:Switch [Enter]:Choose [q]:Exit | | | |

- 3. Press Left arrow key to switch to the Drive field.
- Press the Up/Down arrow keys to find the USB flash disk that contains the latest BIOS, then press <Enter>.
- 5. Press Right arrow key to switch to the Folder Info field.
- 6. Press the Up/Down arrow keys to find the BIOS file, and then press <Enter> to perform the BIOS update process. Reboot the system when the update process is done.



- This function can support devices such as a USB flash disk with FAT 32/16 format and single partition only.
- DO NOT shut down or reset the system while updating the BIOS to prevent system boot failure!



Ensure to load the BIOS default settings to ensure system compatibility and stability. Press <F5> and select **Yes** to load the BIOS default settings.

4.2 BIOS setup program

This motherboard supports a programmable firmware chip that you can update using the provided utility described in section **4.1 Managing and updating your BIOS**.

Use the BIOS Setup program when you are installing a motherboard, reconfiguring your system, or prompted to "Run Setup." This section explains how to configure your system using this utility.

Even if you are not prompted to use the Setup program, you can change the configuration of your computer in the future. For example, you can enable the security password feature or change the power management settings. This requires you to reconfigure your system using the BIOS Setup program so that the computer can recognize these changes and record them in the CMOS RAM of the firmware chip.

The firmware chip on the motherboard stores the Setup utility. When you start up the computer, the system provides you with the opportunity to run this program. Press during the Power-On Self-Test (POST) to enter the Setup utility; otherwise, POST continues with its test routines.

If you wish to enter Setup after POST, restart the system by pressing <Ctrl>+<Alt>+<Delete>, or by pressing the reset button on the system chassis. You can also restart by turning the system off and then back on. Do this last option only if the first two failed.

The Setup program is designed to make it as easy to use as possible. Being a menu-driven program, it lets you scroll through the various sub-menus and make your selections from the available options using the navigation keys.



- The default BIOS settings for this motherboard apply for most conditions to ensure optimum performance. If the system becomes unstable after changing any BIOS settings, load the default settings to ensure system compatibility and stability. Press <F5> and select **Yes** to load the BIOS default settings.
- Support for BIOS functions and options may vary based on AVL testing progress. Please contact your sales representative for more information.
- The BIOS setup screens shown in this section are for reference purposes only, and may not exactly match what you see on your screen.
- Visit the ASUS website (www.asus.com) to download the latest BIOS file for this motherboard.

4.2.1 BIOS menu screen

| lenu items | Menu bar Confi | figuration fields General help | |
|---|---|---|--|
| Main Advanced Platform C | Aptio Setup – A onfiguration Socket Confi | AMI iguration Security Boot Tool | |
| BIOS Information BIOS Vendor Core Version Compliancy BIOS Version Build Date Access Level | American Megatre 5.25 UEFI 2.8; PI 1.7 0214 x64 04/13/2022 Administrator | Choose the system default language 7 | |
| BMC Firmware Revision RC Version | 1.00.05 74.D02 | | |
| Processor Information Type | Genuine Intel(R) |) CPU 00 ++: Select Screen 11: Select Item | |
| Speed | 1500MHz | Enter: Select +/-: Change Opt. | |
| Memory Information Total Memory | 64 GB | F1: General Help F2: Previous Values | |
| Memory Frequency | 4800 MHz | F5: Optimized Defaults | |
| | [English] | F12: Print Screen | |
| System Date System Time | [Tue 05/03/2022] [14:58:06] | | |
| | | | |

Navigation keys

4.2.2 Menu bar

The menu bar on top of the screen has the following main items:

| Main | For changing the basic system configuration |
|------------------------|---|
| Advanced | For changing the advanced system settings |
| Platform Configuration | For configuring the platform settings |
| Socket Configuration | For configuring the socket settings |
| Security | For changing the security settings |
| Boot | For changing the system boot configuration |
| Tool | For configuring options for special functions |
| Save & Exit | For selecting the exit options |

To select an item on the menu bar, press the right or left arrow key on the keyboard until the desired item is highlighted.

4.2.3 Menu items

The highlighted item on the menu bar displays the specific items for that menu. For example, selecting **Main** shows the Main menu items.

The other items (such as Advanced) on the menu bar have their respective menu items.

4.2.4 Submenu items

A solid triangle before each item on any menu screen means that the item has a submenu. To display the submenu, select the item then press <Enter>.

4.2.5 Navigation keys

At the bottom right corner of a menu screen are the navigation keys for the BIOS setup program. Use the navigation keys to select items in the menu and change the settings.

4.2.6 General help

At the top right corner of the menu screen is a brief description of the selected item.

4.2.7 Configuration fields

These fields show the values for the menu items. If an item is user-configurable, you can change the value of the field opposite the item. You cannot select an item that is not user-configurable.

A configurable field is enclosed in brackets, and is highlighted when selected. To change the value of a field, select it and press <Enter> to display a list of options.

4.2.8 Pop-up window

Select a menu item and press <Enter> to display a pop-up window with the configuration options for that item.

4.2.9 Scroll bar

A scroll bar appears on the right side of a menu screen when there are items that do not fit on the screen. Press the Up / Down arrow keys or <Page Up> / <Page Down> keys to display the other items on the screen.

4.3 Main menu

When you enter the BIOS Setup program, the Main menu screen appears. The Main menu provides you an overview of the basic system information, and allows you to set the system date, time, and language settings.

| Aptio Setup – AMI Main Advanced Platform Configuration Socket Configuration Security Boot Tool ▶ | | | | | |
|---|--|---|--|--|--|
| BIOS Information BIOS Vendor Core Version Compliancy BIOS Version Build Date Access Level | American Megatrends 5.25 UEFI 2.8; PI 1.7 0214 x64 04/13/2022 Administrator | Choose the system default language | | | |
| BMC Firmware Revision RC Version | 1.00.05 74.D02 | | | | |
| Processor Information | Genuine Intel(R) CPU AA | | | | |
| . 360 | 00%@ | 14: Select Item | | | |
| Speed | 1500MHz | Enter: Select | | | |
| Memory Information | | F1: General Help | | | |
| Total Memory | 64 GB | F2: Previous Values | | | |
| Memory Frequency | 4800 MHz | F5: Optimized Defaults | | | |
| | | F10: Save Changes & Reset F12: Print Screen ESC: Exit | | | |
| System Date | [Tue 05/03/2022] | | | | |
| System Time | [14:58:06] | | | | |
| Version 2 22 1283 Conwright (C) 2022 AWT | | | | | |

System Language [English]

Allows you to select the system default language.

System Date [Day xx/xx/xxxx]

Allows you to set the system date.

System Time [xx:xx:xx]

Allows you to set the system time.

4.4 Performance Tuning menu

The Performance Tuning menu items allow you to change performance related settings for different scenarios.

| Main Performance Tuning Advanced | Aptio Setup – AMI Platform Configuration | Socket Configuration Security |
|---|---|---|
| Optimized Performance Setting Core Optimizer Engine Boost Power Balancer | [Default] [Disabled] [Disabled] [Disabled] | Dynamically adjusts the frequency of all CPU cores based on their current utilization, delivering better performance mer watt for |

Optimized Performance Setting [Default]

Allows you to select performance settings for different scenarios.

[Default] Default settings.

[By Benchmark] Optimize for different kinds of benchmarks. Select this option, then select a benchmark type from the >> list.

[By Workload] Optimize for different kinds of workloads. Select this option, then select a workload type from the >> list.



This function will reset some BIOS settings that you have changed back to their default values. Please check your BIOS settings again.

Core Optimizer [Disabled]

Allows you to keep the processor operating at the turbo highest frequency for the maximum performance.

Configuration options: [Disabled] [Auto] [Manual]



The following item appears only when you set Core Optimizer to [Manual].

CPU Max frequency [XXXX]

The default value for this option will be the maximum supported frequency of the CPU installed and may vary between different CPUs.

Engine Boost [Disabled]

Enable this item to boost the CPU's frequency. Recommended operation at an ambient temperature of 25°C or below for optimized performance. Configuration options: [Disabled] [Normal] [Aggressive]



Operate with an ambient temperature of 25°C or lower for optimized performance.

Power Balancer [Disabled]

Allows you to dynamically adjust the frequency of all CPU cores based on their current utilization, delivering better performance per watt for improved system energy efficiency. Configuration options: [Disabled] [Enabled by BIOS] [Enabled by ACC]



When setting **Power Balancer** to **[Enabled by ACC]**, make sure that you have the latest ASUS Control Center software installed to support Power Balancer. Please see below for recommended software versions:

- ACC: 1.4.3.5 version or above.



The following item appears only when you set **Power Balancer** to **[Enabled by BIOS]** or **[Enabled by ACC]**.

Policy [Auto]

Configuration options: [Auto] [Manual]



The following item appears only when you set Policy to [Manual].

CPU Max frequency [XXXX]

The default value for this option will be the maximum supported frequency of the CPU installed and may vary between different CPUs.

4.5 Advanced menu

The Advanced menu items allow you to change the settings for the CPU and other system devices.



Take caution when changing the settings of the Advanced menu items. Incorrect field values can cause the system to malfunction.

AutoVMD [Enabled]

[Enabled] Enables VROC (NVMe RAID) function if Intel's Volume Management Device (VMD) feature supports it.

[Disabled] Disables VROC (NVMe RAID) function if Intel's VMD feature does not support it.

| | He feet and the second second second | Ap | tio Setup | - AMI | | | | |
|---------------------|--|--------------|-----------|--------------|--|---|---|--|
| | Main Advanced Platform L | onfiguration | Socket U | onfiguration | Security | Boot | 1001 | |
| * * * * * * * * * * | AutoVMD Trusted Computing ACPI Settings Redfish Host Interface Sett Doboard LAN Configuration Serial Port Console Redired SID Configuration PCI Subsystem Settings USB Configuration Network Stack Configuration APM Configuration | (En tion | | | AutoVMD | | | |
| • | TIS Auth Configuration Intel(R) Virtual RAID on Cf | U | | | ++: Sele +1: Sele Enter: S +/-: Cha F1: Gene F2: Prev F5: Opti F10: Sav F12: Pri ESC: Exi | ct Scre ct Iter elect nge Op† ral He ious Va mized [e Chang nt Scre t | een n Lp llues Defaults ges & Reset een | |
| | | | 1283 Copy | | | | | |

4.5.1 Trusted Computing

| Advanced | Aptio Setup – AMI | |
|--|-------------------|---|
| Configuration Security Device Support NO Security Device Found | [Enable] | Enables or Disables BIOS support for security device. O.S. will not show Security Device. TGG EFI protocol and |

Security Device Support [Enable]

Allows you to enable or disable the BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. Configuration options: [Disable] [Enable]

4.5.2 ACPI Settings

| Advanced | Aptio Setup – AMI | |
|--------------------------------|-------------------|-------------------------------|
| ACPI Settings | | Enables or Disables BIOS ACPI |
| Enable ACPI Auto Configuration | | Huto configuration. |

Enable ACPI Auto Configuration [Disabled]

Allows you to enable or disable the BIOS ACPI Auto Configuration. Configuration options: [Disabled] [Enabled]

4.5.3 Redfish Host Interface Settings

| Advanced | Aptio Setup – AMI | |
|---------------------------------|-------------------|----------------------------|
| Redfish Host Interface Settings | | Enable/Disable AMI Redfish |
| Redfish | | |

Redfish [Disabled]

Allows you to enable or disable Redfish. Configuration options: [Disabled] [Enabled]

The following items appear only when Redfish is set to [Enabled].

Authentication mode [Basic Authentication]

Allows you to select the authentication mode. Configuration options: [Basic Authentication] [Session Authentication]

Redfish BMC Settings

IP address

Allows you to enter the IP address.

IP Mask address

Allows you to enter the IP Mask address.

IP Port

Allows you to enter the IP Port.

4.5.4 Onboard LAN Configuration

The items in this submenu will differ depending on the LAN controller installed on the system.

| Advanced | Aptio Setup – AMI | |
|----------------------------------|-------------------|--------------------------------|
| ▶ Onboard I350 LAN Configuration | | Onboard I350 LAN Configuration |



The following item appears only when an Intel $^{\odot}$ X710-AT2 LAN controller is installed on the system.

Onboard X710 LAN Configuration

Intel X710 LAN1

LAN Enable [JumperState]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [JumperState]



The following item appears only when LAN Enable is set to [JumperState].

Enable OPROM

Allows you to enable or disable X710 option ROM. Configuration options: [Disabled] [Enabled]

Intel X710 LAN2

LAN Enable [JumperState]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [Enabled]



The following item appears only when LAN Enable is set to [JumperState].

Enable OPROM

Allows you to enable or disable X710 option ROM. Configuration options: [Disabled] [Enabled]



The following item appears only when an Intel^{\otimes} I350-AM4 LAN controller is installed on the system.

Onboard I350 LAN Configuration

Intel I350 LAN1

LAN Enable [Enabled]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [Enabled]

Intel I350 LAN2

LAN Enable [Enabled]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [Enabled]

Intel I350 LAN3

LAN Enable [Enabled]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [Enabled]

Intel I350 LAN4

LAN Enable [Enabled]

Allows you to enable or disable the Intel LAN. Configuration options: [Disabled] [Enabled]

4.5.5 Serial Port Console Redirection

| | | Aptio Setup – AMI | |
|----|---|-------------------|---|
| | Advanced | | |
| | COM1 Console Redirection ≻ Console Redirection Settings | [Disabled] | Console Redirection Enable or Disable. |
| • | COM2 Console Redirection ► Console Redirection Settings | [Disabled] | |
| | Console Redirection | Port Is Disabled | |
| L | Serial Port for Out-of-Band Manag | gement/ | |
| L | Windows Emergency Management Serv | vices (EMS) | ++: Select Screen |
| I. | Console Redirection EMS | [Enabled] | T4: Select Item |
| ľ | Console Redirection Settings | | +/-: Change Ont |
| L | | | F1: General Help |
| | | | F2: Previous Values |
| | | | F5: Optimized Defaults |

COM1/COM2

Console Redirection [Disabled]

Allows you to enable or disable the console redirection feature. Configuration options: [Disabled] [Enabled]



The following item appears only when **Console Redirection** for **COM1** or **COM2** is set to **[Enabled]**.

Console Redirection Settings

These items become configurable only when you enable the **Console Redirection** item. The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

Terminal Type [ANSI]

Allows you to set the terminal type.

[VT100] ASCII char set.

[VT100+] Extends VT100 to support color, function keys, etc.

[VT-UTF8]Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.

[ANSI] Extended ASCII char set.

Bits per second [115200]

Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.

Configuration options: [9600] [19200] [38400] [57600] [115200]

Data Bits [8]

Configuration options: [7] [8]

Parity [None]

A parity bit can be sent with the data bits to detect some transmission errors. [Mark] and [Space] parity do not allow for error detection.

[None] None

[Even] Parity bit is 0 if the num of 1's in the data bits is even.

[Odd] Parity bit is 0 if num of 1's in the data bits is odd.

[Mark] Parity bit is always 1.

[Space] Parity bit is always 0.

Stop Bits [1]

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning.) The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

Configuration options: [1] [2]

Flow Control [None]

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals. Configuration options: [None] [Hardware RTS/CTS]

VT -UTF8 Combo Key Support [Enabled]

This allows you to enable the VT -UTF8 Combination Key Support for ANSI/VT100 terminals.

Configuration options: [Disabled] [Enabled]

Recorder Mode [Disabled]

With this mode enabled only text will be sent. This is to capture Terminal data. Configuration options: [Disabled] [Enabled]

Resolution 100x31 [Enabled]

This allows you enable or disable extended terminal solution. Configuration options: [Disabled] [Enabled]

Putty Keypad [VT100]

This allows you to select the FunctionKey and Keypad on Putty. Configuration options: [VT100] [LINUX] [XTERMR6] [SC0] [ESCN] [VT400]

Legacy Console Redirection Settings

Redirection COM Port [COM1]

Allows you to select a COM port to display redirection of Legacy OS and Legacy OPROM Messages. Configuration options: [COM1] [COM2]

Resolution [80x24]

This allows you to set the number of rows and columns supported on the Legacy OS. Configuration options: [80x24] [80x25]

Redirection After POST [Always Enable]

The default setting for this option is set to [Always Enable].

[Bootloader] The legacy Console Redirection is disabled before booting to legacy OS.

[Always Enable] Legacy Console Redirection is enabled for legacy OS.

Serial Port for Out-of-Band Management/ Windows Emergency Management Services (EMS)

Console Redirection EMS [Enabled]

Allows you to enable or disable the console redirection feature. Configuration options: [Disabled] [Enabled]



The following item appears only when Console Redirection EMS is set to [Enabled].

Console Redirection Settings

Out-of-Band Mgmt Port [COM1]

Microsoft Windows Emergency Management Services (EMS) allow for remote management of a Windows Server OS through a serial port. Configuration options: [COM1] [COM2]

Terminal Type EMS [VT-UTF8]

VT-UTF8 is the preferred terminal type for out0of-band management. The next best choice is VT100+, and then VT100. See above, in Console Redirection Settings page for more help with Terminal Type/Emulation. Configuration options: [VT100] [VT100+] [VT-UTF8] [ANSI]

Bits per second EMS [115200]

Selects serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds. Configuration options: [9600] [19200] [57600] [115200]

Flow Control EMS [None]

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a "stop" signal can be sent to stop the data flow. Once the buffers are empty, a "start" signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

Configuration options: [None] [Hardware RTS/CTS] [Software Xon/Xoff]

4.5.6 SIO Configuration

| | Aptio Setup – AMI Advanced | |
|---|--|--|
| Γ | AMI SIO Driver Version : A5.17.00 | View and Set Basic properties |
| | Super IO Chip Logical Device(s) Configuration [*Activex] Serial Port 1 [*Activex] Serial Port 2 | of the SIO Logical device. Like IO Base, IRQ Range, DMA Channel and Device Mode. |
| | WARNING: Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes | |



Logical Devices state on the left side of the control, reflects the current Logical Device state. Changes made during Setup Session will be shown after you restart the system.

[*Active*] Serial Port 1 / [*Active*] Serial Port 2

Allows you to view and set basic properties of the SIO Logical device. Like IO Base, IRQ Range, DMA Channel, and Device Mode.

Use This Device [Enabled]

Allows you to enable or disable this Logical Device. Configuration options: [Disabled] [Enabled]

The following item appears only when Use This Device is set to [Enabled].



Disabling SIO Logical Devices may have unwanted side effects. PROCEED WITH CAUTION.

Possible: [Use Automatic Settings]

Allows the user to change the device resource settings. New settings will be reflected no this setup page after system restarts.

Configuration options: [Use Automatic Settings] [IO=3F8h; IRQ=4; DMA;] [IO=3F8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA;] [IO=2F8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA;] [IO=3E8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA;] [IO=2E8h; IRQ=3, 4, 5, 7, 9, 10, 11, 12; DMA;] DMA;]

4.5.7 PCI Subsystem Settings

Allows you to configure PCI, PCI-X, and PCI Express Settings.

| Advanced | Aptio Setup – AMI | |
|------------------------------|-------------------|--|
| PCI Bus Driver Version | A5.01.27 | Enables or Disables 64bit capable Devices to be Decoded |
| PCI Devices Common Settings: | | in Above 4G Address Space |
| Above 4G Decoding | | (Only if System Supports 64 |
| Re-Size BAR Support | [Disabled] | bit PCI Decoding). |
| SR-IOV Support | [Enabled] | |
| BME DMA Mitigation | [Disabled] | |

Above 4G Decoding [Enabled]

Allows you to enable or disable 64-bit capable devices to be decoded in above 4G address space. It only works if the system supports 64-bit PCI decoding. Configuration options: [Disabled] [Enabled]



The following items appear only when Above 4G Decoding is set to [Enabled].

Re-Size BAR Support [Disabled]

If system has Resizable BAR capable PCIe Devices, this option enables or disables Resizable BAR Support. (Only if system supports 64-bit PCI Decoding). Configuration options: [Disabled] [Auto]



To enable Re-Size BAR Support for harnessing full GPU memory, please set CSM (Compatibility Support Module) to [Disabled].

SR-IOV Support [Enabled]

Allows you to enable or disable Single Root IO Virtualization Support if the system has SR-IOV capable PCIe devices.

Configuration options: [Disabled] [Enabled]

BME DMA Mitigation [Disabled]

Allows you to enable or disable BME DMA mitigation. Re-enable Bus Mater Atttribute disabled during PCI enumeration for PCI bridges after SMM locked. Configuration options: [Disabled] [Enabled]

4.5.8 USB Configuration

| Advanced | Aptio Setup – AMI | |
|--|-------------------------------------|--|
| USB Configuration USB Controllers: 1 XHCI USB Devices: 1 Drive, 1 Keyboard | | This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver. |
| XHCI Hand-off USB Mass Storage Driver Support USB Keyboard and Mouse Simulator | [Enabled] [Enabled] [Enabled] | |
| Mass Storage Devices: JetFlashTranscend 4GB 8.07 | [Auto] | ++: Select Screen |

XHCI Hand-off [Enabled]

Allows you to enable or disable workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver. Configuration options: [Enabled] [Disabled]

USB Mass Storage Driver Support [Enabled]

Allows you to enable or disable the USB Mass Storage driver support. Configuration options: [Disabled] [Enabled]

USB Keyboard and Mouse Simulator [Enabled]

Allows you to simulate USB keyboard and mouse to PS/2 module in Windows 7. Configuration options: [Disabled] [Enabled]

Mass Storage Devices: [Auto]

Allows you to select the mass storage device emulation type for devices connected. [Auto] enumerates devices according to their media format. Optical drives are emulated as [CD-ROM], drives with no media will be emulated according to a drive type. Configuration options: [Auto] [Floppy] [Forced FDD] [Hard Disk] [CD-ROM]

4.5.9 Network Stack Configuration

| Advanced | Aptio Setup – AMI | |
|---|---|--------------------------------------|
| Network Stack IPv4 PXE Support IPv4 HTP Support IPv6 PXE Support IPv6 HTP Support PXE boot wait time Media detect count | [Enabled] [Disabled] [Disabled] [Disabled] [Disabled] 0 1 | Enable/Disable UEFI Network Stack |

Network Stack [Enabled]

Enables or disables the UEFI network stack. Configuration options: [Disabled] [Enabled]



The following items appear only when Network Stack is set to [Enabled].

IPv4 PXE Support [Disabled]

Enables or disables the Ipv4 PXE Boot Support. If disabled, Ipv4 PXE boot support will not be available.

Configuration options: [Disabled] [Enabled]

IPv4 HTTP Support [Disabled]

Enables or disables the Ipv4 HTTP Boot Support. If disabled, Ipv4 HTTP boot support will not be available.

Configuration options: [Disabled] [Enabled]

IPv6 PXE Support [Disabled]

Enables or disables the Ipv6 PXE Boot Support. If disabled, Ipv6 PXE boot support will not be available.XD

Configuration options: [Disabled] [Enabled]

IPv6 HTTP Support [Disabled]

Enables or disables the Ipv6 HTTP Boot Support. If disabled, Ipv6 HTTP boot support will not be available. Configuration options: [Disabled] [Enabled]

DVC hast weit times [0]

PXE boot wait time [0]

Set the wait time to press ESC key to abort the PXE boot. Use the <+> or <-> to adjust the value. The values range from 0 to 5.

Media detect count [1]

Set the number of times presence of media will be checked. Use the <+> or <-> to adjust the value. The values range from 1 to 50.

4.5.10 NVMe Configuration

This page will display the NVMe controller and drive information.



Device



The devices and names shown in the NVMe configuration list depends on the connected devices. If no devices are connected, **No NVMe Device Found** will be displayed.

Self Test Option [Short]

This option allows you to select either Short or Extended Self Test. Short option will take couple of minutes, and the extended option will take several minutes to complete. Configuration options: [Short] [Extended]

Self Test Action [Controller Only Test]

This item allows you to select either to test Controller alone or Controller and NameSpace. Selecting Controller and Namespace option will take a lot longer to complete the test.

Configuration options: [Controller Only Test] [Controller and NameSpace Test]

Run Device Self Test

Press <Enter> to perform device self test for the corresponding Option and Action selected by the user. Pressing the <ESC> key will abort the test. The results shown below is the most recent result logged in the device.

4.5.11 APM Configuration

This page will allow you to configure the Advance Power Management (APM) settings.



Restore AC Power Loss [Last State]

When set to **[Power Off]**, the system goes into off state after an AC power loss. When set to **[Power On]**, the system will reboot after an AC power loss. When set to **[Last State]**, the system goes into either off or on state, whatever the system state was before the AC power loss.

Configuration options: [Power On] [Power Off] [Last State]

Power On By PCI-E/PCI [Disabled]

| [Disabled] | Disables the PCI/PCIe devices to generate a wake event. |
|------------|---|
| [Enabled] | Enables the PCI/PCIe devices to generate a wake event. |

Power On By RTC [Disabled]

 [Disabled]
 Disables RTC to generate a wake event.

 [Enabled]
 When set to [Enabled], the items RTC Alarm Date (Days) and Hour/Minute/Second will become user-configurable with set values.

4.5.12 TIs Auth Configuration

The items in this menu allows you to set the authentication mode.



Server CA Configuration

This option allows you to configure the server's CA. This allows you to both enroll the required certificate files and also to delete the certicates that are not required.

4.5.13 Intel(R) Virtual RAID on CPU

This page will allow you to manage Intel(R) Virtual RAID on CPU.

All Intel VMD Controllers

For more information on the Intel VMD controllers, press <Enter>.

4.6 Platform Configuration menu

The Platform Configuration menu items allow you to change the platform settings.

 \mathbf{A}

Settings items in this menu to incorrect values may cause the system to malfunction!

| Aptio Setup – AMI Main Advanced <mark>Platform Configuration</mark> Socket Configuration S | Security Boot Tool → |
|---|---|
| PCH-IO Configuration Miscellaneous Configuration Server ME Configuration Runtime Error Logging | PCH Parameters |
| Setup Warning: Setting items on this Screen to incorrect values may cause system to malfunction! | |
| | ++: Select Screen tl: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset |
| Version 2.22.1283 Copyright (C) 2022 | HIZ: Frint Screen ESC: Exit AMI |

4.6.1 PCH-IO Configuration



SATA And RST Configuration

This option allows you to make device options settings.

Controller 1/2/3 SATA And RST Configuration

DeepSx Power Policies [Disabled]

This option allows you to enable to disable DeepSx power policies. Configuration options: [Disabled] [Enabled in S5]

IEH Mode [Enabled]

This option allows you to enable or bypass IEH mode. Configuration options: [Bypass Mode] [Enabled]

4.6.2 Miscellaneous Configuration



Active Video [Auto]

Allows you to select the active video type. Configuration options: [Auto] [Onboard Device] [PCIE Device]

4.6.3 Server ME Configuration

Displays the Server ME Technology parameters on your system. Scroll using <Page Up> / <Page Down> keys to see more items.

| Aptio Setup - AMI Platform Configuration | | |
|--|---|---|
| General ME Configuration Oper. Firmware Version Backup Firmware Version Recovery Firmware Status #1 ME Firmware Status #2 Current State Erron Code Recovery Cause Intel ME Target Image Boot Altitude MCTF Bus Owner | 18:6.0.3.44 N/A 18:6.0.3.44 0x00000355 0x88500006 Operational No Error N/A Success 8000 0 | The altitude of the platform location above the sea level, expressed in meters. The hex number is decoded as 2's complement signed integer. Provide the 8000h value if the altitude is unknown. |
| SiEn NodeManager PECIProxy ICC MeStorageServices BootGuard PmBusProxy CpuHotPlug ThermalReport HSIO PECIOverDMI PECIOverDMI PCHDebug | | ++: Select Screen 14: Select Item Enter: Select 4/-: Change Opt, F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen ESC: Exit |

Altitude [8000]

Allows you to set the altitude of the platform location above the sea level, expressed in meters. The hex number is decoded as 2's complement signed integer. Provide the 8000h value if the altitude is unknown.

MCTP Bus Owner [0]

Allows you to enter the MCTP bus owner location on PCIe: [15:8] bus, [7:3] device, [2:0] function. If all zeros sending bus owner will be disabled.

Power Supply Units Configuration

PSU #1/2/3/4

Allows you to enter the PMBus address (7-bit) that will be used to retrieve the status of the PSU. Enter zero to disable query.

4.6.4 Runtime Error Logging



System Errors [Enable]

Allows you to enable or disable System Errors setup options. Configuration options: [Disable] [Enable]



The following items are only available when System Errors is set to [Enable].

Whea Settings

Whea Support [Enable]

Allows you to enable or disable Whea support. Configuration options: [Disable] [Enable]

4.7 Socket Configuration menu

The Socket Configuration menu items allow you to change the socket settings.

| Aptio Setup – AMI Main Advanced Platform Configuration Socket Configuration S | Security Boot Tool 🔹 🕨 |
|--|---|
| Processor Configuration Common RefCode Configuration Uncore Configuration Memory Configuration IIO Configuration IIO Configuration Advanced Power Management Configuration | Displays and provides option to change the Processor Settings |
| | <pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen ESC: Exit</pre> |
| Version 2.22.1283 Copyright (C) 2022 | AMI |

4.7.1 Processor Configuration

Scroll using the <Page Up> / <Page Down> keys to view more items.

| | Aptio Setup - AMI Socket Configuration |
|--|---|
| Processor Configuration | Change Per-Socket Settings |
| Per-Socket Configuration Processor BSP Revision Processor Socket Processor ID Processor Frequency Processor Max Ratio Processor Min Ratio Microcode Revision L1 Cache RAM(Per Core) L2 Cache RAM(Per Core) L3 Cache RAM(Per Package) | 806F3 - SPR-SP Dx Socket 0 Socket 1 000806F3* 000806F3 1.500GH2 1.500GH2 0FH 0FH 08H 08H 8D000450 8D00450 80KB 80KB 2048KB 2048KB 32160KB 92160KB |
| Processor 1 Version | 00%0 Genuine Intel(R) CPU 00 00%0 +/-: Change Opt. F1: Select Item |
| Hyper-Threading IED Trace memory Skip Flex Ratio Override Check CPU BIST Result 3StrikeTimer Fast String Machine Check | [Enabled] F2: Previous Values [Disable] F5: Optimized Defaults [Disabled] F10: Save Changes & Reset [Enabled] F12: Print Screen [Enable] ESC: Exit [Enable] • |

Processor Configuration

Per-Socket Configuration

Allows you to change Per-Socket Settings.

CPU Socket 0/1 Configuration

Core Disable Bitmap(Hex) [0]

Allows you to set the Core Disable Bitmap. Set this item to [0] to enable all cores. Set this item to [FFFFFFFFF] to disable all cores.



At least one core per CPU must be enabled. Disabling all cores is an invalid configuration.

Hyper Threading [Enable]

Allows you to enable or disable the Hyper-Threading Technology function. When disabled, only one thread per activated core is enabled. This is the software method to enable or disable Logical Processor threads.

Configuration options: [Disabled] [Enabled]

IED Trace memory [Disable]

Allows to enable or disable option to allocate memory for PSMI trace. Configuration options: [Disable] [4M] [8M] [16M] [32M] [64M] [128M] [256M] [512M] [1G]
Skip Flex Ratio Override [Disable]

Allows you to use power-on default Flex Ratio values. In multi-socket systems, this will allow mixed flex ratio limits. Configuration options: [Disabled] [Enabled]

Check CPU BIST Result [Enabled]

When enabled, this item disables failed BIST core. Otherwise, it ignores BIST result. Configuration options: [Disable] [Enable]

3StrikeTimer [Enable]

The 3 strike counter can be turned off by writing into the MISC_FEATURE. Configuration options: [Enable] [Disable]

Fast String [Enable]

This item allows you to enable or disable fast strings for REP MOVS/STOS. Configuration options: [Disable] [Enable]

Machine Check [Enable]

This item allows you to enable or disable machine check. Configuration options: [Disable] [Enable]

Hardware Prefetcher [Enable]

Allows you to enable or disable MLC streamer prefetcher (MSR 1A4h Bit[0]). Configuration options: [Disable] [Enable]

L2 RFO Prefetch Disable [Disable]

Allows you to enable or disable L2 RFO prefetcher (MSR 6Dh Bit[35]). Configuration options: [Disable] [Enable]

Adjacent Cache Prefetch [Disable]

Allows you to enable or disable MLC spatial prefetcher (MSR 1A4h Bit[1]). Configuration options: [Enable] [Disable]

DCU Streamer Prefetcher [Enable]

Allows you to enable or disable DCU streamer prefetcher, which is a L1 data cache prefetcher (MSR 1A4h Bit[2]). Configuration options: [Enable] [Disable]

DCU IP Prefetcher [Enable]

Allows you to enable or disable DCU IP prefetcher, which is a L1 data cache prefetcher (MSR 1A4h Bit[3]). Configuration options: [Enable] [Disable]

LLC Prefetch [Disable]

Allows you to enable or disable LLC prefetch on all threads. Configuration options: [Disable] [Enable]

DCU Mode [Normal]

[Normal] The whole DCU is used for caching. [Mirror-Mode] The DCU is organized as 2x16KB mirrored copies.

AMP Prefetch [Disable]

Allows you to enable or disable MLC AMP prefetch (MSR 1A4h [4]). Configuration options: [Enable] [Disable]

Bsp Selection [Auto]

Allows you to choose the method to select BSP. Configuration options: [Socket 0] [Socket 1] [Auto]

Extended APIC [Disable]

Allows you to enable or disable extended APIC support. Configuration options: [Disable] [Enable]



Enabling this item will automatically enable VT-d if x2APIC is enabled.

APIC Physical Mode [Disable]

Allows you to enable or disable APIC physical destination mode. Configuration options: [Disable] [Enable]

PECI Trust Mode [Use per-PECI agent trust mode]

Allows you to set PECI trust configuration. Configuration options: [All PECI Agents untrusted] [All PECI Agents trusted] [Use per-PECI agent trust mode]



The following item appears only when $\ensuremath{\text{PECI}}$ Trust Mode is set to [Use per-PECI agent trust mode].

Legacy Agent [Enable]

Allows you to enable or disable the legacy PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

SMBus Agent [Disable]

Allows you to enable or disable the SMBus PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

IE Agent [Enable]

Allows you to enable or disable the IE PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

Generic Agent [Disable]

Allows you to enable or disable the generic PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

eSPI Agent [Disable]

Allows you to enable or disable the eSPI PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

DfxRedManu Agent [Disable]

Allows you to enable or disable the DfxRedManu PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

DfxOrange Agent [Disable]

Allows you to enable or disable the Orange PECI agent in trust bit enable. Configuration options: [Disable] [Enable]

DBP-F [Disable]

Allows you to enable or disable DBP-F. DBP-F can be turned off by writing into the MSR 6Dh [2:3].

Configuration options: [Enable] [Disable]

IIO LLC Ways [19:0] (Hex) [0]

Allows you to adjust the LLC value of MSR_IIO_LLC_WAYS bitmask.

eSMM Blocked and Delayed [Disable]

Allows you to enable or disable SMM Blocked and Delayed. Configuration options: [Disable] [Enable] DBP-F [Disable]

eSMM Save State [Disable]

Allows you to enable or disable the eSMM Save State Feature. Configuration options: [Disable] [Enable]

SMBus Error Recovery [Enable]

Allows you to enable or disable SMBus Error Recovery. Configuration options: [Disable] [Enable]

Enable Intel(R) TXT [Disable]

Allows you to enable or disable Intel(R) TXT. Configuration options: [Disable] [Enable]

VMX [Enable]

Allows you to enable or disable Vanderpool Technology. Changes take effect only after reboot.

Configuration options: [Disable] [Enable]

Enable SMX [Disable]

Allows you to enable or disable Safer Mode Extensions. Configuration options: [Disable] [Enable]

Lock Chipset [Enable]

Allows you to lock or unlock the chipset. Configuration options: [Enable] [Disable]

MSR Lock Control [Enable]

Allows you to enable or disable the locking of MSR 3Ah and CSR 80h. Power Good reset is needed to remove lock bits. Configuration options: [Disable] [Enable]

PPIN Control [Unlock/Enable]

Allows you to unlock and enable or lock and disable the PPIN control. Configuration options: [Lock/Disable] [Unlock/Enable]

AES-NI [Enable]

Allows you to enable or disable AES-NI support. Configuration options: [Disable] [Enable]

TME, TME-MT, TDX

Memory Encryption (TME) [Disabled]

Allows you to enable or disable memory encryption (TME). Configuration options: [Disabled] [Enabled]



The following item appears only when Memory Encryption (TME) is set to [Enabled].

Total Memory Encryption (TME) Bypass [Disabled]

Allows you to enable or disable Total Memory Encryption (TME). Configuration options: [Disabled] [Enabled]

Software Guard Extension (SGX)

SGX Factory Reset [Disabled]

Allows you to enable or disable SGX factory reset, which deletes all registration date, on the subsequent boot.

Configuration options: [Disabled] [Enabled]



When enabled, the system will force Initial Platform Establishment flow.

SW Guard Extensions (SGX) [Disabled]

Allows you to enable or disable Software Guard Extensions (SGX). Configuration options: [Disabled] [Enabled]

SGX Package Info In-Band Access [Disabled]

Allows you to enable or disable Software Guard Extensions (SGX) package info in-band access.

Configuration options: [Disabled] [Enabled]



The following items appear only when SW Guard Extensions (SGX) is set to [Enabled].

SGX PRM Size [256M]

Allows you to select the SGX PRM size, which may not be equal to the total PRM size. Configuration options: [256M] [512M] [1G] [2G] [4G] [8G] [16G]

SGX QoS [Enabled]

Allows you to enable or disable SGX Qualtiy of Service. Configuration options: [Disabled] [Enabled]

Select Owner EPOCK input type [Manual User Defined Owner EPOCHs]

Allows you to select the owner EPOCH input type. Each EPOCH is 64-bit. Configuration options: [Change to New Randon Owner EPOCHs] [Manual User Defined Owner EPOCHs]

Software Guard Extensions Epoch 0 [67]

Allows you to set Software Guard Extensions Epoch 0.

Software Guard Extensions Epoch 1 [4388E8D3C9E73ADC]

Allows you to set Software Guard Extensions Epoch 1.

SGXLEPUBKEYHASHx Write Enable [Enabled]

Allows you to enable or disable writing to SGXLEPUBKEYHASH [3..0] from PS/SW. Configuration options: [Disabled] [Enabled]



The following items appear only when SGXLEPUBKEYHASHx Write Enable is set to [Enabled].

SGXLEPUBKEYHASH0 [0]

Allows you to input SGX launch enclave public key hash byte 7-0.

SGXLEPUBKEYHASH1 [0]

Allows you to input SGX launch enclave public key hash byte 15-8.

SGXLEPUBKEYHASH2 [0]

Allows you to input SGX launch enclave public key hash byte 23-16.

SGXLEPUBKEYHASH3 [0]

Allows you to input SGX launch enclave public key hash byte 31-24.

SGX Auto MP Registration [Disabled]

Allows you to enable or disable automatic registration in OS MPA agent. Configuration options: [Disabled] [Enabled]

In Field Test (IFT)

Press <Enter> to bring up the In Field Test menu.

Enable SAF [Disabled]

Allows you to enable or disable Scan At Field (SAF) Configuration options: [Disabled] [Enabled]



The following item appears only when Enable SAF is set to [Enabled].

SAF size [128M]

Allows you to set the SAF size region inside of PRM.

PSMI Configuration

Press <Enter> to bring up the PSMI Configuration menu.

Global PSMI Enable [Enable]

Allows you to enable or disable Scan At Field (SAF) Configuration options: [Disabled] [Enabled] [Force setup]



The following items appear only when Global PSMI Enable is set to [Enable] or [Force setup].

Socket 0/1 Configuration

Press <Enter> to bring up the Socket 0/1 Configuration menu.

PSMI Enable

Allows you to enable or disable PSMI Enable. Configuration options: [Disabled] [Enabled]

Processor CFR Configuration

Press <Enter> to bring up the Processor CFR Configuration menu that displays and provides options to change the processor CFR settings.

Provision S3M CFR [Enable]

Allows you to enable or disable provision S3M CFR. Configuration options: [Disabled] [Enabled]



The following item appears only when Provision S3M CFR is set to [Enable].

Manual Commit S3M FW CFR [Disable]

Allows you to enable or disable manual commit S3M FW CFR. Configuration options: [Disabled] [Enabled]

Provision PUcode CFR [Enable]

Allows you to enable or disable provision PUcode CFR. Configuration options: [Disabled] [Enabled]



The following item appears only when Provision PUcode CFR is set to [Enable].

Manual Commit PUcode CFR [Disable]

Allows you to enable or disable manual commit PUcode CFR. Configuration options: [Disabled] [Enabled]

Socket0/1/2/3 CFR Revision Info

These items display the CFR revision info for each socket.

4.7.2 Common RefCode Configuration



Numa [Enabled]

This item allows you to enable or disable Non uniform Memory Access (NUMA). Configuration options: [Disable] [Enable]

Virtual Numa [Disable]

Enabling this option divides physical NUMA nodes into evenly sized virtual NUMA modes in ACPI table. This may improve Windows performance on CPUs with more than 64 logical processors.

Configuration options: [Disable] [Enable]

4.7.3 Uncore Configuration



Uncore General Configuration

This item displays and provides options to change the Uncore General Settings.

Uncore Status

Displays uncore status

MMCFG Base [Auto]

Allows you to select the MMCFG base. Configuration options: [1G] [1.5G] [1.75G] [2G] [2.25G] [3G] [Auto]

MMCFG Size [Auto]

Allows you to select the MMCFG size. Configuration options: [128M] [256M] [512M] [1G] [2G] [Auto]

MMIO High Base [32T]

Allows you to select the MMIO high base. Configuration options: [56T] [40T] [32T] [24T] [16T] [4T] [2T] [1T] [512G] [3584T]

MMIO High Granularity Size [256G]

Allows you to select the allocation size used to assign mmioh resources. Total mmioh space can be up to 32xgranularity. Configuration options: [1G] [4G] [16G] [64G] [256G] [1024G]

Limit CPU PA to 46 bits [Enable]

Allows you to limit CPU physical address to 46 bits to support older Hyper-v. If enabled, automatically disables TME-MT.

Uncore Per Socket Configuration

CPU1/2

CPU 1/2 UPI Port 3 Link Disable [No] Allows you to set UPI link to disable. Configuration options: [No] [Yes]

Current UPI Link Speed [Auto] Allows you to select the UPI link frequency. Configuration options: [12.8GT/s] [14.4GT/s] [16.0GT/s] [Auto]

4.7.4 **Memory Configuration**

| Aptio Setup – AMI Socket Configuration | | |
|---|--|--|
| Integrated Memory Controller (IMC) | | Enforces Plan Of Record restrictions for DDR frequency programming. |
| Enforce DDR Memory Frequency POR Enforce Population POR Memory Frequency Sockets in parallel Attempt Fast Boot Attempt Fast Cold Boot Data Scrambling for PMem Data Commbling for PMem | (POR) [Enable] [Auto] [ALL] [Enable] [Enable] [Auto] [Enable] | |
| ► ADR ► Memory Topology ► Memory Hap ► Memory RAS Configuration ► PMem Configuration | [Enable] | <pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help</pre> |

Enforce DDR Memory Frequency POR [POR]

Allows you to enforce POR restrictions for DDR4 frequency and voltage programming. If this item is set to [Disable], user will be able to run at higher frequencies, specified in the DDR Frequency Limit field (limited by processor support). Configuration options: [POR] [Disable]

Enforce Population POR [Enable]

Allows you to enable or disable memory population POR enforcement. Configuration options: [Disable] [Enable]

Memory Frequency [Auto]

Allows you to select the maximum memory frequency setting in Mhz. If Enforce POR is set to [Disable], user will be able to run at higher frequencies than the memory support (limited by processor support). Do not select Reserved.

Configuration options: [Auto] [3200] [3600] [4000] [4400] [4800]

Sockets in parallel [ALL]

Allows you to set how many sockets are executing at any time. Configuration options: [ALL] [1] [2] [4]

- [ALL] All sockets operate in parallel.
- [1] At any one time, only one socket is executing.
- [2] At any one time, only two sockets are executing.
- [4] At any one time, only four sockets are executing.

Attempt Fast Boot [Enable]

- [Enable] Portions of memory reference code will be skipped when possible to increase boot speed on warm boots.
- Disables this feature. [Disable]
- [Auto] Sets it to the MRC default setting

Attempt Fast Cold Boot [Enable]

| [Enable] | Portions of memory reference code will be skipped when possible to |
|----------|--|
| | increase boot speed on cold boots. |

[Disable] Disables this feature.

[Auto] Sets it to the MRC default setting

Data Scrambling for PMem [Auto]

| [Enable] | Enables data scrambling for DDR5 and DDR5 |
|-----------|---|
| [Disable] | Disables this feature. |
| [Auto] | Sets depending on stepping. |

Data Scrambling for DDR5/5 [Enable]

| [Disable] | Disables this feature. |
|-----------|--|
| [Enable] | Enables data scrambling for DDR5 and DDR5. |

Enable ADR [Enable]

[Disable] This option is not available if fADS is enabled since fADR requires ADR to be enabled.

[Enable] Enables the detecting and enabling of ADR.

Memory Topology

Displays memory topology with DIMM population information.

Memory Map

Allows you to set memory mapping settings.

Volatile Memory Mode [2LM]

Selects 1LM or 2LM mode for volatile memory. For 2LM memory mode, BIOS will try to configure 2LM, but if BIOS is unable to configure 2LM, volatile memory mode will fall beck to 1LM.

Configuration options: [1LM] [2LM]

Memory RAS Configuration

Displays and provides options to change the memory RAS Settings.

Dynamic ECC Mode Selection [Enabled]

Allows you to enable or disable dynamic ECC mode selection. Configuration options: [Disabled] [Enabled] [Enable + Allow 128b ECC]

Enable Pcode WA for SAI PG [Disabled]

Allows you to enable or disable Pcode Work Around for SAI Policy group for A Step. Configuration options: [Disabled] [Enabled]

Mirror Mode [Disabled]

Allows you to select the mirror mode. Configuration options: [Disabled] [Full Mirror Mode] [Partial Mirror Mode]

Mirror TADO [Disabled]

Allows you to enable or disable mirror on entire memory for TADO. Configuration options: [Enabled] [Disabled]

UEFI ARM Mirror [Disabled]

Allows you to enable or disable the mirrored behavior of UEFI based address range mirror with setup option. Configuration options: [Disabled] [Enabled]

Memory Correctable Error Flood Policy [Frequency]

Allows you to set when to trigger SMI. Configuration options: [Disabled] [Once] [Frequency]

Trigger SW Error Threshold [Disabled]

Allows you to enable to disable sparing trigger SW error match threshold. Configuration options: [Disabled] [Enabled]

Leaky bucket time window based interface [Disabled]

Allows you to enable to disable leaky bucket time window based interface. Configuration options: [Disabled] [Enabled]

ADDDC Sparing [Disabled]

Allows you to enable to disable ADDDC Sparing. Configuration options: [Disabled] [Enabled]



The following item appears only when ADDDC Sparing is set to [Enabled].

Enable ADDDC Error Injection [Enabled]

Allows you to enable to disable ADDDC error injection, which is required forcing the interleave granularity to 64B for B/L step parts. Configuration options: [Disabled] [Enabled]

Patrol Scrub [Enable at End of POST]

Allows you to enable to disable patrol scrub. Configuration options: [Disabled] [Enable at End of POST]

PDDR5 ECS [Disabled]

Allows you to enable to disable DDR5 Error Check and Scrub (ECS). Configuration options: [Disabled] [Enabled]

PMem Configuration

PMem Secure Erase Unit

Erase All DIMMs [Disable]

Allows you to erase the persistent memory region of all PMem in the system.

S0 CH0-7 [Disable]

Allows you to enable or disable the erasing of the persistent memory region of PMem in channel 0-7 for socket 0.

S1 CH0-7 [Disable]

Allows you to enable or disable the erasing of the persistent memory region of PMem in channel 0-7 for socket 1.

S2 CH0-7 [Disable]

Allows you to enable or disable the erasing of the persistent memory region of PMem in channel 0-7 for socket 2.

S3 CH0-7 [Disable]

Allows you to enable or disable the erasing of the persistent memory region of PMem in channel 0-7 for socket 3.

PMem Factory Reset/Clear [Disabled]

This option allows you to enable or disable factory reset/clear. PMem power policy settings will also be set to default values. This know remains enabled after reset. Configuration options: [Disabled] [Enabled]

200 Series PMem Average Power Limit (in mW) [0]

This option allows you to set the power limit in mW used for average power. Valid range for power limit starts from 10000mW and must be a multiple of 250mW.

200 Series PMem Turbo/Memory Bandwidth Boost (MBB) Feature [Enabled]

This option allows you to enable or disable Turbo/Memory Bandwidth Boost (MBB) feature on the module.

Configuration options: [Disabled] [Enabled]

200 Series PMem Memory Bandwidth Boost (MBB) Average Power Time Constant (in mSec) [15000]

This option allows you to set the value used as a base time window for power usage measurements. Valid range starts from 1000msec to 120000msec (must be in increments of 1000msec).

200 Series PMem Turbo/Memory Bandwidth Boost (MBB) Power Limit (in mW) [18000]

This option allows you to set the power limit in mW used for limiting the Turbo/Memory Bandwidth Boost (MBB) feature power consumption. Valid range for Turbo Power Limit starts from 15000mW.

Publish ARS capability [Enabled]

This option allows you to enable or disable publishing the Address Range Scrub capability to the OS.

Configuration options: [Disabled] [Enabled]

PMem ECC Read Check [Enable]

This option allows you to enable or disable PMem ECC Read Check. Configuration options: [Disable] [Enable]

PMem Latch System Shutdown State [Enabled]

This option allows you to enable or disable latch system shutdown state. Configuration options: [Disabled] [Enabled]

Snoopy mode for 2LM [Disable]

This option allows you to enable or disable new 2LM specific feature to avoid directory updates to far-memory from non-NUMA optimized workloads. Configuration options: [Enable] [Disable]

Power Cycle on PMem Surprise Clock Stop [Enable]

This option allows you to enable or disable power cycle policy when PMem receive surprise clock stop. Configuration options: [Enable] [Disable]

Snoopy mode for AD [Disable]

This option allows you to enable or disable new AD specific feature to avoid directory updates to PMem memory from non-NUMA optimized workloads. Configuration options: [Disable] [Enable]

LSx implementation [ASL]

This option allows you to select LSI/LSR/LSW ACPI method implementation. Configuration options: [SWSMI] [ASL]

Extended Type 17 Structure [Enabled]

This option allows you to enable or disable the use extended Type 17 SMBIOS Structures.

Configuration options: [Disabled] [Enabled]

SMBus Max Access Time [350]

This option allows you to set the maximum amount of time (ms) UEFI mgmt driver is allowed to use the SMBus.

SMBus Release Delay [150]

This option allows you to set the delay time (ms) before releasing after UEFI mgmt driver requests SMBus release.

NVDIMM Mailbox in NFIT [Disabled]

This option allows you to enable or disable publishing NVDIMM mailbox registers in NFIT structures.

Configuration options: [Disabled] [Enabled]

Seamless: Opt-in DIMMs [Keep]

This option allows you to enable opt-in DIMMs, if FW supports it. Configuration options: [Keep] [Disabled] [Enabled]



FW activation does not update FW digest CSR.

4.7.5 IIO Configuration

| Aptio Setup – AMI Socket Configuration | | |
|---|--|---|
| IIO Configuration | | Press <enter> to bring up the Intel® Virtualization for Directed I/O (VT-d)</enter> |
| ▶ Intel® VT for Directed I/O (VT–d) | | Configuration menu. |

Intel® VT for Directed I/O (VT-d)

 ${\sf Press}$ <Enter> to bring up the Intel® Virtualization Technology for Directed I/O (VT-d) configuration menu.

Intel® VT for Directed I/O [Enable]

This item allowsw you to enable or disable Intel® Virtualization Technology for Directed I/O (VT-d) by reporting the I/O device assihnment to VMM through DMAR ACPI Tables. Configuration options: [Enable] [Disable]

4.7.6 Advanced Power Management Configuration



CPU P State Control

P State Control Configuration Sub Menus, including Turbo, XE, etc.

AVX Licence Pre-Grant Override [Disable]

Enabled AVX ICCP pre-grant level override. Configuration options: [Disable] [Enable]



The following item appears only when **AVX Licence Pre-Grant Override** is set to [Enable].

AVX ICCP pre-grant level [128 Heavy]

Pre-grants an AVX level to the core. Base frequency is not updated. Configuration options: [128 Heavy] [256 Light] [256 Heavy] [512 Light] [512 Heavy]

AVX P1 [Normal]

AVX P1 level selection. Configuration options: [Normal] [Level 1] [Level 2]

Intel SST-PP [Auto]

Intel SST-PP allows user to choose level. Configuration options: [Auto] [Level 0] [Level 3] [Level 4]

Dynamic SST-PP [Disable]

Enabling Dynamic SST-PP supports dynamic SST-PP selection. Configuration options: [Disable] [Enable]

Activate SST-BF [Disable]

Allows you to enable or disable SST-BF. Configuration options: [Disable] [Enable]

Configure SST-BF [Enable]

Allows BIOS to configure SST-BF High Priority Cores so that SW does not have to configure. Configuration options: [Disable] [Enable]

SpeedStep (Pstates) [Enable]

Allows you to enable or disable EIST (P-States). Configuration options: [Disable] [Enable]

Boot performance mode [Max Performance]

Allows you to select the performance state that the BIOS will set before OS hand off. Configuration options: [Max Performance] [Max Efficient] [Set by Intel Node Manager]

Energy Efficient Turbo [Enable]

Allows you to enable or disable Energy Efficient Turbo. Configuration options: [Disable] [Enable]

Hardware PM State Control

Hardware P-States [Native Mode]

 Allows you to switch between Hardware P-States mode.

 [Disable]
 Hardware chooses a P-state based on OS Request (Legacy P-States).

 [Native Mode]
 Hardware chooses a P-state based on OS guidance.

 [Out of Band Mode]
 Hardware autonomously chooses a P-state (no OS guidance).

 [Native Mode with no Legacy Support]
 Hardware chooses a P-state based on OS guidance).

Frequency Prioritization

SST-CP [Enable]

Allows you to enable or disable SST-CP. When enabled, per core power budgeting is activated. Configuration options: [Enable] [Disable]

CPU C State Control

Enable Monitor MWAIT [Auto]

Allows you to enable or disable Monitor and MWAIT instructions. Configuration options: [Disable] [Enable] [Auto]

CPU C1 auto demotion [Enable]

Allows CPU to automatically demote to C1. Takes effect after reboot. Configuration options: [Disable] [Enable]

CPU C1 auto undemotion [Enable]

Allows CPU to automatically undemote from C1. Takes effect after reboot. Configuration options: [Disable] [Enable]

CPU C6 report [Auto]

Allows you to enable or disable CPU C6 (ACPI C3) report to OS. Configuration options: [Disable] [Enable] [Auto]

Enhanced Halt State (C1E) [Enable]

Core C1E auto promotion Control. Takes effect after reboot. Configuration options: [Disable] [Enable]

OS ACPI Cx [ACPI C2]

Allows you to select to report CC3/CC6 to OS ACPI C2 or ACPI C3. Configuration options: [ACPI C2] [ACPI C3]

Package C State Control

Package C State [Auto]

Allows you to select Package C State limit. Configuration options: [C0/C1 state] [C2 state] [C6(non Retention state] [C6(Retention state] [No Limit] [Auto]

4.8 Security menu

This menu allows a new password to be created or a current password to be changed. The menu also enables or disables the Secure Boot state and lets the user configure the System Mode state.

| Main Advanced Platform Configura | Aptio Setup – AMI ation Socket Configuration | Security Boot Tool 🛛 |
|--|--|--|
| Password Description If ONLY the Administrator's passwor | d is set, then this only | To clear the administrator password, key in the current password in the Enter Current |
| limits access to Setup and is only Setup. If ONLY the User's password is set | asked for when entering then this is a power on | Password box, and then press <enter> when prompted to create/confirm the password.</enter> |
| password and must be entered to bo In Setup the User will have Adminis | ot or enter Setup. strator rights. | |
| The password length must be in the is a power on password and must be beat op option Satur. In Satur the l | following range: entered to | |
| have Administrator rights. The password length must be | 561. MIII | |
| in the following range: | | ++: Select Screen |
| Minimum length | 3 | t∔: Select Item |
| Maximum length | 20 | Enter: Select +/−: Change Opt. |
| | | F1: General Help |
| User Password | | F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen FSC: Fxit |
| ▶ Secure Boot | | |
| | | |
| Version | 2 22 1283 Copyright (C) 2022 | AMT |

Administrator Password

To set an administrator password:

- 1. Select the Administrator Password item and press < Enter>.
- 2. From the Create New Password box, key in a password, then press <Enter>.
- 3. Confirm the password when prompted.

To change an administrator password:

- 1. Select the Administrator Password item and press <Enter>.
- 2. From the Enter Current Password box, key in the current password, then press <Enter>.
- 3. From the Create New Password box, key in a new password, then press < Enter>.
- 4. Confirm the password when prompted.



To clear the administrator password, follow the same steps as in changing an administrator password, but press <Enter> when prompted to create/confirm the password.

User Password

To set a user password:

- 1. Select the User Password item and press <Enter>.
- 2. From the Create New Password box, key in a password, then press <Enter>.
- 3. Confirm the password when prompted.

To change a user password:

- 1. Select the User Password item and press <Enter>.
- 2. From the Enter Current Password box, key in the current password, then press <Enter>.
- 3. From the Create New Password box, key in a new password, then press <Enter>.
- 4. Confirm the password when prompted.

To clear a user password:

- 1. Select the Clear User Password item and press < Enter>.
- 2. Select Yes from the Warning message window then press <Enter>.

4.8.1 Secure Boot

This item allows you to customize the Secure Boot settings.

| | Aptio Setup - AMI | Security |
|--|--------------------------|---|
| Secure Boot state | User | Secure Boot feature is Active if Secure Boot is Enabled, |
| Secure Boot | [Disabled] Not Active | Platform Key(PK) is enrolled and the System is in User mode. The mode change requires |
| Secure Boot Mode ▶ Install Default Secure Boot Keys ▶ Clear Secure Boot Keys | (Custom) | platform reset |
| ► Key Management | | |
| | | ++: Select Screen |

Secure Boot [Disabled]

Secure Boot feature is active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the system is in User mode. The mode change requires platform reset. Configuration options: [Disabled] [Enabled]

Secure Boot Mode [Custom]

Allows you to set the Secure Boot selector. In Custom mode, Secure Boot Policy variables can be configured physically by the present user without full authentication. Configuration options: [Custom] [Standard]



The following items are available only when Secure Boot Mode is set to [Custom].

Install Default Secure Boot Keys

This option will load the default secure boot keys, including the PK (Platform key), KEK (key-exchange key), db (signature database), and dbx (revoked signature database). All the secure boot keys states will change from unloaded to loaded. Save changes and reset the system for the changes to take effect.

Clear Secure Boot Keys

This option will delete all previously applied secure boot keys, including the PK (Platform key), KEK (key-exchange key), db (signature database), and dbx (revoked signature database). All the secure boot keys states will change from unloaded to loaded. Save changes and reset the system for the changes to take effect.

Key Management

This item only appears when the item **Secure Boot Mode** is set to **[Custom]**. The Key Management item allows you to modify Secure Boot variables and set Key Management page.

Factory Key Provision [Enabled]

Allows you to provision factory default Secure Boot keys when the system is in Setup Mode.

Configuration options: [Disabled] [Enabled]

Install Default Secure Boot Keys

This option will load the default secure boot keys, including the PK (Platform key), KEK (key-exchange key), db (signature database), and dbx (revoked signature database). All the secure boot keys states will change from unloaded to loaded. Save changes and reset the system for the changes to take effect.

Clear Secure Boot Keys

This option will delete all previously applied secure boot keys, including the PK (Platform key), KEK (key-exchange key), db (signature database), and dbx (revoked signature database). All the secure boot keys states will change from unloaded to loaded. Save changes and reset the system for the changes to take effect.

PK Management

Configuration options: [Details] [Save To File] [Set New Key] [Delete key]

KEK Management / DB Management / DBX Management

Configuration options: [Details] [Save To File] [Set New Key] [Append Key] [Delete key]

DB Management / DB Management / DBX Management Configuration options: [Details] [Save To File] [Set New Key] [Append Key] [Delete key]

DBX Management / DB Management / DBX Management

Configuration options: [Details] [Save To File] [Set New Key] [Append Key] [Delete key]

Authorized TimeStamps / OsRecovery Signatures

Configuration options: [Set New Key] [Append Key]

Save all Secure Boot Variables

This option will save NVRAM content of Secure Boot policy variables to the file (EFI_ SIGNATURE_LIST data format) in root foler on a target file system device.

Enroll Efi Image

This item will allow the image to run in Secure Boot mode. Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db).

4.9 Boot menu

The Boot menu items allow you to change the system boot options.

| Main Advanced Platform Configurat. | Aptio Setup – AMI ion Socket Configuration | Security Boot Tool |
|---|---|---|
| Boot Configuration Setup Promot Timeout Bootup NumLock State Boot Logo Display | 5 [On] [Disabled] | Number of seconds to wait for setup activation key. 65535(0xFFF) means indefinite waiting. |
| Boot Option Priorities Boot Option #1 | [UEFI: JetFlashTranscend 46B 8.07, Partition 1] | |
| | | ++: Select Screen ++: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen ESC: Exit |
| Version 2 | .22.1283 Copyright (C) 2022 | AMI |

Setup Prompt Timeout [5]

Allows you to set the number of seconds that the firmware waits before initiating the original default boot selection. 65535(OxFFFF) means indefinite waiting. Use the <+> or <-> to adjust the value.

Bootup NumLock State [On]

Allows you to select the power-on state for the NumLock. Configuration options: [Off] [On]

Boot Logo Display [Disabled]

[Disabled] Hide the logo during POST.

[Enabled] Display the logo during POST.

Boot Option Priorities

These items specify the boot device priority sequence from the available devices. The number of device items that appears on the screen depends on the number of devices installed in the system.



- To select the boot device during system startup, press <F11> when logo appears.
- To access Windows OS in Safe Mode, please press <F8> after POST.

4.10 Tool menu

The Tool menu items allow you to configure options for special functions. Select an item then press <Enter> to display the submenu.

| Ap Nain Advanced Platform Configuration | otio Setup – AMI Socket Configuration | Security Post Teel |
|--|--|---|
| | SUCKET CONTIGURATION | Security Boot 1001 |
| Start ASUS EzFlash IPMI Hardware Monitor ASUS SMBIOS Viewer ASUS Storage Viewer | | Press ENTER to run the utility to select and update BIOS. |
| | | <pre>++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen ESC: Exit</pre> |
| Version 2.22. | 1283 Copyright (C) 202 | 2 AMI |

Start ASUS EzFlash

Allows you to run ASUS EzFlash BIOS ROM Utility when you press <Enter>. Refer to the ASUS EzFlash Utility section for details.

IPMI Hardware Monitor

Allows you to run the IPMI hardware monitor.

ASUS SMBIOS Viewer

Allows you to start ASUS SMBIOS Viewer when you press <Enter>.

ASUS Storage Viewer

Allows you to start ASUS Storage Viewer when you press <Enter>.

4.11 Event Logs menu

The Event Logs menu items allow you to change the event log settings and view the system event logs.



4.11.1 Change Smbios Event Log Settings

Press <Enter> to view all smbios event logs.

| Event Logs | Aptio Setup – AMI | |
|--|-------------------------|--|
| Enabling/Disabling Options Smbios Event Log | [Enabled] | Change this to enable or disable all features of Smbios Event Logging during boot. |
| Erasing Settings Erase Event Log When Log is Full | [No] [Do Nothing] | |
| Custom Options Log EFI Status Code Convert EFI Status Codes to Standard Smbios Type | [Enabled] [Disabled] | |

All values changed here do not take effect until computer is restarted.

Enabling/Disabling Options

Smbios Event Log [Enabled]

Change this to enable or disable all features of Smbios Event Logging during boot. Configuration options: [Disabled] [Enabled]



The following items only appear when Smbios Event Log is set to [Enabled].

Erasing Settings

Erase Event Log [No]

Choose options for erasing Smbios Event Log. Erasing is done prior to any logging activation during reset.

Configuration options: [No] [Yes, Next reset] [Yes, Every reset]

When Log is Full [Do Nothing]

Choose options for reactions to a full Smbios Event Log. Configuration options: [Do Nothing] [Erase Immediately]

Custom Options

Log EFI Status Code [Enabled]

Allows you to enable or disable the logging of EFI Status Codes as OEM reserved type E0 (if not already converted to legacy).

Configuration options: [Disabled] [Enabled]



The following item only appears when Log EFI Status Code is set to [Enabled].

Convert EFI Status Codes to Standard Smbios Type [Disabled]

Allows you to enable or disable the converting of EFI Status Codes to Standard Smbios Types (not all may be translated).

Configuration options: [Disabled] [Enabled]

4.11.2 View Smbios Event Log

Press <Enter> to view all smbios event logs.

| Event Logs | Apt | tio Setup – AMI | |
|-------------------------------|-------------|-------------------|---------------------------|
| DATE TIME ERROR CODE | SEVERITY | COUNT | DESCRIPTION |
| 04/27/22 17:09:48 Smbios 0v16 | | | Log in cu hoset |
| 05/03/22 14:54:39 EEI 0305100 | Maior | 01 | |
| 05/26/22 13:49:36 EEI 0305100 | Major | 01 | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| | | | ++: Select Screen |
| | | | T∔: Select Item |
| | | | Enter: Select |
| | | | +/-: Change Opt. |
| | | | F1: General Help |
| | | | F2: Previous Values |
| | | | F5: Uptimized Defaults |
| | | | F10: Save Changes & Reset |
| | | | F12: Print Screen |
| | | | ESU: EXIT |
| | | | |
| | | | |
| Ver | sion 2.22.: | 1283 Copyright ((| C) 2022 AMI |

4.12 Server Mgmt menu

The Server Management menu displays the server management status and allows you to change the settings.

| ✓ Event Logs Server Mgmt Exit | Aptio Setup – AMI | |
|---|---|--|
| BMC Self Test Status BMC Device ID BMC Device Revision BMC Firmware Revision IPMI Version OS Watchdog Timer OS Wtd Timer Timeout OS Wtd Timer Policy Serial Mux | PASSED 32 81 1.00.05 2.0 [Disabled] 10 [Reset] [Disabled] | If enabled, starts a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine that the OS successfully loaded or follows the OS Boot Watchdog Timer policy. |
| System Event Log BMC network configuration View System Event Log | | ++: Select Screen TJ: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F5: Optimized Defaults F10: Save Changes & Reset F12: Print Screen ESC: Exit |
| Version 2 | .22.1283 Copyright (C) 2022 | AMI |

OS Watchdog Timer [Disabled]

This item allows you to start a BIOS timer which can only be shut off by Management Software after the OS loads. Helps determine if the OS successfully loaded or follows the OS Boot Watchdog Timer policy.

Configuration options: [Disabled] [Enabled]

The following items are available only when OS Watchdog Timer is set to [Enabled].

OS Wtd Timer Timeout [10]

Allows you to enter a value between 1 to 30 minutes for OS Boot Watchdog Timer Expiration. Not available if OS Boot Watchdog Timer is disabled. Configuration options: [1] - [30]

OS Wtd Timer Policy [Reset]

This item allows you to configure the how the system should respond if the OS Boot Watch Timer expires. Not available if OS Boot Watchdog Timer is disabled. Configuration options: [Do Nothing] [Reset] [Power Down] [Power Cycle]

Serial Mux [Disabled]

Allows you to enable or disable Serial Mux configuration. Configuration options: [Disabled] [Enabled]

4.12.1 System Event Log

Allows you to change the SEL event log configuration.

```
      All values changed here do not take effect until computer is restarted.

      Aptio Setup - AMI

      Server Mgmt

      Change this to enable or disable event logging for error/progress codes during boot.

      Erasing Settings Erase SEL [No]

      NOTE: All values changed here do not take effect until computer is restarted.
```

SEL Components [Enabled]

Allows you to enable or disable event logging for error/progress codes during boot. Configuration options: [Disabled] [Enabled]



The following item is available only when SEL Components is set to [Enabled].

Erase SEL [No]

Allows you to choose options for erasing SEL. Configuration options: [No] [Yes, On next reset] [Yes, On every reset]

4.12.2 BMC network configuration

The sub-items in this configuration allow you to configure the BMC network parameters. Scroll using <Page Up> / <Page Down> keys to see more items.

| BMC network configuration Select to configure LAN BMC network configure IPv4 support Channel parameters statically Configure IPv4 support channel parameters statically www.www.www.www.www.www.www.www.www.ww | Server Mgmt | Aptio Setup – AMI | |
|---|-------------------------------|-----------------------|-------------------------------|
| ************************************ | BMC network configuration | | ▲ Select to configure LAN |
| Configure IPv4 support or dynamically(by BIOS or xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx | жжжжжжжжже | | channel parameters statically |
| ************************************ | Configure IPv4 support | | or dynamically(by BIOS or |
| DM_LAN Inot modify ang BMC network DM_LAN Denflumation Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Subret mask 0.0.0.0 Subnet mask 0.0.0.0 Router IP address 0.0.0.0 Shared LAN [Unspecified] Current Configuration Address source [Unspecified] Furrent Configuration Address 0.0-00-00-00-00 Shared LAN [Unspecified] Current Configuration Address DynamicAddressBmcDhcp source Station IP address Station IP address 192.168.1.165 Station MAC address 00-3F-4A-AP-24-22 Figuration Address 00-3F-4A-AP-24-22 Figuration Address 00-3F-4A-AP-24-22 Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AP-24-22 Station MAC address 00-3F-4A-AP-24-22 Station MAC address 00-3F-4A-AP-24-22 Station MAC address 00-3F-4A-AP-24-22 Station MAC address 192.168.1.16 | жжжжжжжжжж | | BMC). Unspecified option will |
| DM_LAN parameters during EIDS phase Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Subnet mask 0.0.0.0 Station IP address 0.0.0.0 Station MAC address 0.0.0.0 Router IP address 0.0.0.0 Shared LAN [Unspecified] Current Configuration Address D0-00-00-00-00 Shared LAN [Unspecified] Current Configuration Address DynamicAddressBmcDhcp source [Unspecified] current Configuration Address DynamicAddressBmcDhcp source [Station IP address 192.168.1.165 Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AP-24-22 Fi0: Save Changes & Reset F12: Print Screen Station MAC address 00-3F-4A-AP-24-22 Station MAC address 00-3F-4A-AP-24-22 Fi0: Save Changes & Reset F12: Print Screen Station MAC address 192.168.1.1 | | | not modify any BMC network |
| Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Source Station IP address 0.0.0.0 Station IP address 0.0.0.0 Station MAC address 00-8F-4A-AA-24-76 Router IP address 0.0.0.0 Router IP address 0.0-00-00-00 Shared LAN | DM_LAN | | parameters during BIOS phase |
| Current Configuration Address DynamicAddressBmcDhcp source 0.0.0.0 Subnet mask 0.0.0.0 Station MAC address 0.0.0.0 Station MAC address 0.0.0.0 Router IP address 0.0.0.0 Router MAC address 00-07F-4A-AA-24-76 Router MAC address 00-00-00-000 **: Select Screen 11: Select Item Shared LAN Enter: Select Current Configuration Address source [Unspecified] F1: General Help F2: Previous Values Station IP address 192.168.1.165 Station MAC address 00-3F-4A-AA-24-22 F10: Save Changes & Reset F12: Print Screen Station MAC address 192.168.1.1 ESC: Exit ESC: Exit | Configuration Address source | | |
| source 0.0.0.0 Station IP address 0.0.0.0 Subnet mask 0.0.0.0 Station MAC address 00-3F-4A-AA-24-76 Router IP address 0.0.0.0 Bouter MAC address 0.0.0.0 Shared LAN Enter: Select Item Configuration Address source [Unspecified] current Configuration Address DynamicAddressBmcDhcp source 5: Optimized Defaults Station IP address 192.168.1.165 Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AA-24-22 Fi0: Save Changes & Reset Fi2: Ife: Provide Set and the set | Current Configuration Address | DynamicAddressBmcDhcp | |
| Station IP address 0.0.0.0 Subnet mask 0.0.0.0 Station MAC address 00-3F-4A-AA-24-76 Router IP address 0.0.0.0 Router IP address 0.0.0.0 Shared LAN It: Select Item Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Station IP address 192.168.1.165 Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AA-24-22 Fit: Select Screen Fit: General Help F: Previous Values F5: Optimized Defaults Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AA-24-22 Station P address 192.168.1.16 | source | | |
| Subnet mask 0.0.0.0 Station MAC address 00-3F-4A-AA-24-76 Router IP address 0.0.0.0 Router MAC address 00-00-00-00 Shared LAN 11: Select Item Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Station IP address 192.168.1.165 Station MAC address 00-3F-4A-AA-24-22 Fi: General Help F2: Previous Values Station MAC address 00-3F-4A-AA-24-22 Fiu: Save Changes & Reset Fiu: Print Screen Router IP address 192.168.1.1 ESC: Exit ESC: Exit | Station IP address | 0.0.0.0 | |
| Station MAC address 00-3F-4A-AA-24-76 Router IP address 0.0.0 Router MAC address 00-00-00-00 Shared LAN It: Select Item Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Station IP address 192.168.1.165 Station IP address 00-3F-4A-AA-24-22 Station MAC address 00-3F-4A-AA-24-22 Fi: General Help FS: Optimized Defaults Subnet mask 255.255.255.0 Station MAC address 00-3F-4A-AA-24-22 Fi: Station MAC address 192.168.1.1 | Subnet mask | 0.0.0.0 | |
| Router IP address 0.0.0.0 Router MAC address 00-00-00-00-00 Shared LAN ++: Select Screen Configuration Address source [Unspecified] Current Configuration Address DynamicAddressBmcDhcp Station IP address 192.168.1.165 Station MAC address 00-3F-4A-AA-24-22 Station IP address 192.168.1.16 | Station MAC address | 00-3F-4A-AA-24-76 | |
| Router MAC address 00-00-00-00-00 ++: Select Screen Shared LAN T1: Select Item Configuration Address source [Unspecified] +/-: Change Opt. Current Configuration Address DynamicAddressBmcDhcp F1: General Help Source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Previous Values | Router IP address | 0.0.0.0 | |
| Shared LAN T1: Select Item Configuration Address source [Unspecified] Enter: Select Current Configuration Address DynamicAddressBmcDhcp F1: General Help source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Router MAC address | 00-00-00-00-00 | ++: Select Screen |
| Shared LAN Enter: Select Configuration Address source [Unspecified] +/-: Change Opt. Current Configuration Address DynamicAddressBmcDhcp F1: General Help source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.166.1.1 ESC: Exit | | | T+: Select Item |
| Configuration Address source [Unspecified] +/-: Change Opt. Current Configuration Address DynamicAddressBmcDhcp F1: General Help Source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Shared LAN | | Enter: Select |
| Current Configuration Address DynamicAddressBmcDhcp F1: General Heip source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Configuration Address source | [Unspecified] | +/-: Change Opt. |
| source F2: Previous Values Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Current Configuration Address | DynamicAddressBmcDhcp | F1: General Help |
| Station IP address 192.168.1.165 F5: Optimized Defaults Subnet mask 255.255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | source | | F2: Previous Values |
| Subnet mask 255.255.0 F10: Save Changes & Reset Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Station IP address | 192.168.1.165 | F5: Optimized Defaults |
| Station MAC address 00-3F-4A-AA-24-22 F12: Print Screen Router IP address 192.168.1.1 ESC: Exit | Subnet mask | 255.255.255.0 | F10: Save Changes & Reset |
| Router IP address 192.168.1.1 ESC: Exit | Station MAC address | 00-3F-4A-AA-24-22 | F12: Print Screen |
| | Router IP address | 192.168.1.1 | ESC: Exit |
| Router MAC address 10-C3-78-51-14-70 | Router MAC address | 10-C3-7B-51-14-70 | |

Configure IPv4 support

DM_LAN1 / Shared LAN

Configuration Address source [Unspecified]

Allows you to set the LAN channel parameters statically or dynamically (by BIOS or by BMC). [Previous State] option will not modify any BMC network parameters during BIOS phase.

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]



The following items are available only when **Configuration Address source** is set to **[Static]**.

Station IP address

Allows you to set the station IP address.

Subnet mask

Allows you to set the subnet mask. We recommend that you use the same Subnet Mask you have specified on the operating system network for the used network card.

Router IP Address

Allows you to set the router IP address.

Router MAC Address

Allows you to set the router MAC address.

Configure IPv6 support

DM_LAN1 / Shared LAN

IPV6 support [Enabled]

Allows you to enable or disable IPV6 support. Configuration options: [Enabled] [Disabled]



The following items appear only when IPV6 support is set to [Enabled].

Configuration Address source [Previous State]

Allows you to set the LAN channel parameters statically or dynamically (by BIOS or by BMC). [Previous State] option will not modify any BMC network parameters during BIOS phase.

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]



The following items are available only when **Configuration Address source** is set to **[Static]**.

Station IPV6 address

Allows you to set the station IPV6 address.

Prefix Length

Allows you to set the prefix length (maximum of Prefix Length is 128).

Configuration Router LAN1/2 Address [Previous State]

Allows you to set the LAN channel parameters statically or dynamically (by BIOS or by BMC). Unspecified option will not modify any BMC network parameters during BIOS phase.

Configuration options: [Previous State] [Static] [DynamicBmcDhcp]



The following items are available only when **Configuration Router LAN1/2 Address** is set to [Static].

IPV6 Router1 IP Address

Allows you to set the IPV6 Router1 IP address.

IPV6 Router1 Prefix Length Lan1/2

Allows you to set the IPV6 router prefix length (maximum of IPV6 Router Prefix Length is 128).

IPV6 Router1 Prefix Value Lan1/2

Allows you to change the IPV6 router prefix value.

4.12.3 View System Event Log

This item allows you to view the system event log records. Scroll using <Page Up> / <Page Down> keys to see more items.

| Server Mgmt | Aptio Setup – AMI |
|---------------------------------|---|
| No. of log entries in SEL : 100 | HEX: |
| DATE TIME SENSOR TYPE | 01 00 02 20 00 5A 62 20 00 04 01 02 01 57 52 51 |
| 04/16/22 14:05:32 Temperature | Generator ID: BMC – LUN #0 |
| 04/16/22 14:07:20 Temperature | (Channel #0) |
| 04/27/22 17:46:09 Fan | Sensor Number: 0x02 CPU2 |
| 04/27/22 17:46:09 Fan | Temperatur |
| 04/27/22 17:46:09 Fan | Event Description: Record |
| 04/27/22 17:46:09 Fan | Type-0x02. Assertion Event. |
| 04/27/22 17:46:17 Fan | |
| 04/27/22 17:46:17 Fan | |
| 04/27/22 17:46:19 Fan | · · · · · · · · · · · · · · · · · · · |
| 04/27/22 17:46:19 Fan | ++: Select Screen |
| 04/27/22 17:46:26 Fan | ↑↓: Select Item |
| 04/27/22 17:46:26 Fan | Enter: Select |
| 04/27/22 17:46:29 Fan | +/-: Change Opt. |
| 04/27/22 17:46:29 Fan | F1: General Help |
| 04/27/22 17:48:33 Temperature | F2: Previous Values |
| 04/27/22 17:49:02 Temperature | F5: Optimized Defaults |
| 04/27/22 17:49:09 Fan | F10: Save Changes & Reset |
| 04/27/22 17:49:09 Fan | F12: Print Screen |
| 04/27/22 17:49:09 Fan | ESC: Exit |
| 04/27/22 17:49:09 Fan | |
| 04/27/22 17:49:09 Fan | × |

4.13 Exit menu

The Exit menu items allow you to save or discard your changes to the BIOS items.





Pressing <Esc> does not immediately exit this menu. Select one of the options from this menu or <F10> from the legend bar to exit.

Discard Changes and Exit

Exit system setup without saving any changes.

Save Changes and Reset

Reset system after saving the changes.

Discard Changes and Reset

Reset system setup without saving any changes.

Save Changes

Save changes done so far to any of the setup options.

Discard Changes

Discard changes done so far to any of the setup options.

Load Optimized Defaults

Load optimized default values for all the setup options.

Boot Override

This item displays the available devices. The device items that appears on the screen depends on the number of devices installed in the system. Click an item to start booting from the selected device.



5

Driver Installation

This chapter provides instructions for installing the necessary drivers for different system components.

Running the Support DVD

The support DVD that is bundled with your motherboard contains drivers, management applications, and utilities that you can install to maximize the features of your motherboard.

- The contents of the support DVD are subject to change at any time without notice. Visit the ASUS website (<u>www.asus.com</u>) for the latest updates on software and utilities.
 - The support DVD is supported on Windows® Server 2016 and Windows® Server 2019.

The main screen of the Support DVD contains the following tabs:

- 1. Drivers Shows the available device drivers that the system detects.
- 2. Utilities Displays the software applications and utilities that the motherboard supports.
- 3. Manual Provides the link to the user guide(s).



You need an internet browser installed in your OS to view the User Guide.

 Contact - Displays the ASUS contact information, e-mail addresses, and useful links if you need more information or technical support for your motherboard.
Appendix

This appendix includes additional information that you may refer to when configuring the motherboard.

Z13PP-D32 block diagram



Notices

Federal Communications Commission Statement

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- This device may not cause harmful interference.
- This device must accept any interference received including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.



The use of shielded cables for connection of the monitor to the graphics card is required to assure compliance with FCC regulations. Changes or modifications to this unit not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

Compliance Statement of Innovation, Science and Economic Development Canada (ISED)

This device complies with Innovation, Science and Economic Development Canada licence exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

CAN ICES-003(B)/NMB-003(B)

Déclaration de conformité de Innovation, Sciences et Développement économique Canada (ISED)

Le présent appareil est conforme aux CNR d'Innovation, Sciences et Développement économique Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

CAN ICES-003(B)/NMB-003(B)

Australia statement notice

From 1 January 2012 updated warranties apply to all ASUS products, consistent with the Australian Consumer Law. For the latest product warranty details please visit https://www.asus.com/support/. Our goods come with guarantees that cannot be excluded under the Australian Consumer Law. You are entitled to a replacement or refund for a major failure and compensation for any other reasonably foreseeable loss or damage. You are also entitled to have the goods repaired or replaced if the goods fail to be of acceptable quality and the failure does not amount to a major failure.

If you require assistance please call ASUS Customer Service 1300 2787 88 or visit us at https://www.asus.com/support/.



DO NOT throw the motherboard in municipal waste. This product has been designed to enable proper reuse of parts and recycling. This symbol of the crossed out wheeled bin indicates that the product (electrical and electronic equipment) should not be placed in municipal waste. Check local regulations for disposal of electronic products.



DO NOT throw the mercury-containing button cell battery in municipal waste. This symbol of the crossed out wheeled bin indicates that the battery should not be placed in municipal waste.

Japan statement notice

This product cannot be directly connected to the Internet (including public wireless LAN) of a telecom carrier (mobile network companies, landline network companies, Internet providers, etc.). When connecting this product to the Internet, be sure to connect it through a router or switch.

Japan JATE

本製品は電気通信事業者(移動通信会社、固定通信会社、インターネットプロバイダ等)の通信回線(公衆無線LANを含む)に直接接続することができません。本製品をインターネットに接続する場合は、必ずルータ等を経由し接続してください。」等が考えられる。

Safety Precautions

Accessories that came with this product have been designed and verified for the use in connection with this product. Never use accessories for other products to prevent the risk of electric shock or fire.

安全上のご注意

付属品は当該専用品です。他の機器には使用しないでください。機器の破損もしくは、火災や感電 の原因となることがあります。

Declaration of compliance for product environmental regulation

ASUS follows the green design concept to design and manufacture our products, and makes sure that each stage of the product life cycle of ASUS product is in line with global environmental regulations. In addition, ASUS disclose the relevant information based on regulation requirements.

Please refer to http://csr.asus.com/Compliance.htm for information disclosure based on regulation requirements ASUS is complied with:

EU REACH and Article 33

Complying with the REACH (Registration, Evaluation, Authorization, and Restriction of Chemicals) regulatory framework, we publish the chemical substances in our products at ASUS REACH website at http://csr.asus.com/english/REACH.htm.

EU RoHS

This product complies with the EU RoHS Directive. For more details, see http://csr.asus.com/english/article.aspx?id=35

Japan JIS-C-0950 Material Declarations

Information on Japan RoHS (JIS-C-0950) chemical disclosures is available on http://csr.asus.com/english/article.aspx?id=19

India RoHS

This product complies with the "India E-Waste (Management) Rules, 2016" and prohibits use of lead, mercury, hexavalent chromium, polybrominated biphenyls (PBBs) and polybrominated diphenyl ethers (PBDEs) in concentrations exceeding 0.1% by weight in homogenous materials and 0.01% by weight in homogenous materials for cadmium, except for the exemptions listed in Schedule II of the Rule.

Vietnam RoHS

ASUS products sold in Vietnam, on or after September 23, 2011, meet the requirements of the Vietnam Circular 30/2011/TT-BCT.

Các sản phẩm ASUS bán tại Việt Nam, vào ngày 23 tháng 9 năm2011 trở về sau, đều phải đáp ứng các yêu cầu của Thông tư 30/2011/TT-BCT của Việt Nam.

Türkiye RoHS

AEEE Yönetmeliğine Uygundur

ASUS Recycling/Takeback Services

ASUS recycling and takeback programs come from our commitment to the highest standards for protecting our environment. We believe in providing solutions for you to be able to responsibly recycle our products, batteries, other components as well as the packaging materials. Please go to http://csr.asus.com/english/Takeback.htm for detailed recycling information in different regions.

Ecodesign Directive

European Union announced a framework for the setting of ecodesign requirements for energy-related products (2009/125/EC). Specific Implementing Measures are aimed at improving environmental performance of specific products or across multiple product types. ASUS provides product information on the CSR website. The further information could be found at https://csr.asus.com/english/article.aspx?id=1555.

Service and Support

Visit our multi-language website at https://www.asus.com/support/

